

# Paralleled DC Boost Converters with Feedback Control using PSO Optimization Technique for Photovoltaic Module Application

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## ABSTRACT

In this paper a novel method of controller design for boost type dc-dc converter is proposed. DC/DC converters are widely used in photovoltaic generating systems as an interface between the photovoltaic panel and the load. Therefore, a better possible control technique is required in order to control the variation in output voltage of DC/DC converter due to the variation occurring in the external dynamics input parameters such as radiation, temperature and internal impedance of the photovoltaic (PV) module. In this paper, two paralleled DC/DC converter with a closed loop PWM based control is simulated to obtain constant output voltage. The optimal values of feedback PID controller are obtained using Particle Swarm Optimization Algorithm (PSOA). Extensive simulation result is found out with linear controller parameters and the same are presented here. Here comparison of the output of the PSOA based design and design of PID controller with transient performance specification (T-PID) for under-damped system is done. The PSO based tuning of PID controller is much better as determined by the simulation results.

## General Terms

Transient performance specification, Particle Swarm Optimization Algorithm.

## Keywords

DC/DC Converter, PSO-PID, Pulse width modulation, solar energy.

## 1. INTRODUCTION

Solar energy, which is available free of cost and is abundant for long hours during daytime and with high intensity throughout the year, especially in Gujarat, India. To capture these abundant, free solar energy very efficiently, various efforts are being deployed in the fields of material science of photovoltaic, power electronics and control automation all over the globe. The solar energy market is experiencing a remarkable rapid growth since last decade, in the areas of widespread use from solar portable devices; electric vehicles (EV) to standalone system and up to the interactive PV connected grid system. A latest lesson learned from Germany is of 21 PV systems in operation for 10 years, exposed that inverters contributed for 63% of failures, PV modules 15% and other components 23%, with a failure occurring, on an average, every 4.5 years [1]. In order to reduce the high failure rates of PV balance of system (BOS) and directly the cost of these power system, research uses the mathematical models for the analysis of developed system performances. One of the important systems of BOS is the DC/DC converter which may get transformed into DC transformer for the DC micro-grid application in future. The paralleling of switching converters based on power supply contributes many advantages when compared with a single high power

centralized power supply [2]. Utilization of the DC low voltage opens new possibilities for network distribution development and low voltage residential application. A DC Micro-grid can reduce AC to DC conversion losses from an average of 32% to 10% [3, 4].

The DC/DC converters are nonlinear dynamic systems. The primary reasons for the non-linearity are due to high frequency switching, power devices like MOSFETs, diodes and passive components such as inductors and capacitors. Therefore, there is a need for an optimal control technique for these DC/DC converters which can deal with their intrinsic nonlinearity and wide variations in the input voltage and load variations ensuring stability in any operating condition while taking care of in obtaining the fast transient response.

In [5] this review paper, it is concluded that the best type of converter among the non-isolated DC/DC converter for PV system is the buck-boost DC/DC converter. To ensure PV module operation point always at MPP, DC/DC converters are used [6]. In another paper a complete control system that uses two DC/DC converters to control the operating power of the electrolyser and the fuel cell is presented. The simulation results of PID controller and a fuzzy logic controller to manage the flow of energy in the system were almost same and since the PI controller is simpler to implement, it was retained as the best option[7].In [8] the conventional PID controlled DC/DC converter is discussed and it is proved that by using BFOA-PID the peak overshoot of the response is very much reduced and its settling time is very less, robustness in its stability is also improved when subjected to external disturbances. A non-isolated boost converter is connected with the PV module at its input side and at its output a load is connected which is to be driven. The results published in [9] indicate that the performance of the fuzzy controller is superior to that of the linear PID and PI controllers. The fuzzy controller is able to achieve more stable steady-state response, faster transient response, and highly robust under different operating points. In [10] this paper, a novel genetic algorithm based smart-PID controller for optimal control of DC-DC boost converter used as voltage controller in PV systems. It maximizes the stable operating range by using genetic algorithms (GA) to tune the PID parameters at various loading conditions.

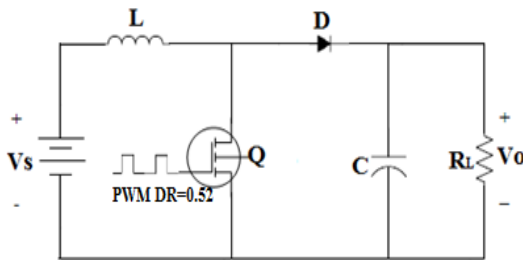
The digital simulation results presented in [11] concluded that the enhanced PSO algorithm has stable convergence characteristic and good computation ability, and it is an effective method for optimal tuning of PID gains. In order to improve the performance of the controller, a modified PSO method is proposed and experiments confirm the method can improve the performance of PID controller dramatically [12]. In this paper boost converter is used to step up the low variable voltage from the PV module and provide the high quality, regulated DC voltage. Section 2 describes about the design of DC converter and in section 3, the PID controller

optimization using two different methods is discussed. In section 4 two similar closed loop DC-DC boost converters are paralleled and simulated results are discussed.

## 2. DESIGN OF BOOST CONVERTER:

In this section a conventional non-isolated DC/DC boost converter operating in continuous current mode is discussed. The MOSFET switch and diode is assumed to be ideal and the passive electronics elements such as capacitor and inductor are ideal with no parasitic resistances. The simplified form of DC/DC boost converter is shown in Fig 1.

In [13] a boost converter operating in continuous current mode which is able to draw maximum power from the PV system for a given irradiation level by adjusting the duty cycle of the converter is discussed.



**Fig 1: Conventional PWM Switching Boost DC/DC Converter**

The ideal dynamics of the boost converter are derived by the state space averaging method. The boost converter, step up the input voltage to a voltage equal to input and higher value. The relationship between the input voltage ( $V_s$ ) and the output voltage ( $V_o$ ) is given [14] as:

$$M_v = \frac{V_o}{V_s} = \frac{1}{1-D} \quad (1)$$

Where,  $D = \frac{T_{on}}{T_s}$ , is the duty ratio,  $T_s$  is the switching period,  $T_{on}$  is conducting time of the switch,  $R_L$ , is the load resistance and  $f_s$  is the switching frequency. The mode of operation of the boost converter in CCM mode is decided by the positive values of inductor current. The boundary condition between CCM and DCM of the Boost converter is dependent on the critical value of the inductor  $L$  and is given in [14] as:

$$L = \frac{T_s V_o}{I_{oB}} D(1-D)^2 \quad (2)$$

Where  $I_{oB}$  is the average value of the output current and values of  $D$  and  $V_o$  is constant. The converter to operate in CCM mode should have larger value then the critical inductor value. This inductor value determines the magnitude of ripple current in the output capacitor as well as the load current. The value of the components used in the boost converter are  $L$  is 18mH,  $C$  is 980 $\mu$ F, load resistor  $R_L$  value is 26 $\Omega$ , switching

frequency 100kHz and the duty ratio is 0.52. The transfer function of the boost converter is given by equation 3

$$T.F = \frac{V_o}{V_{in}} = \frac{\left(\frac{1}{LCR_L}\right)}{S^2 + \frac{S}{LCR_L} + \frac{1}{LC}} = \frac{2.18 \times 10^3}{S^2 + 2.18 \times 10^3 S + 5.67 \times 10^4} \quad (3)$$

## 3. DESIGN OF PID CONTROLLER:

The performance of a closed loop converter is greatly influenced by controller parameters. The controller ensures stable operation of the converter. In practice, PID is the most common used controller for the control of DC/DC converters due to their adoption in all other closed loop systems [15,16]. The PID control provides control signal which is relative to the error between the reference signal and the actual output, to the integral of the error and to the derivative of the error. The general equation of control signal for a PID controller is as follows:

$$u(t) = K_p \left[ e(t) + \frac{1}{T_i} \int_0^t e(\tau) d\tau + T_d \frac{d}{dt} e(t) \right] \quad (4)$$

Where  $u(t)$  and  $e(t)$  denote the control and error signals,  $K_p$ ,  $T_i$  and  $T_d$  are the parameters to be tuned. The transfer function is given as

$$K(s) = K_p \left( 1 + \frac{1}{T_i s} + T_d s \right) \quad (5)$$

Two different algorithms are used for tuning the gain parameters of the PID controller and their results are compared. The particle swarm optimization algorithm (PSO) for PID tuning and tuning design of PID as proposed in [17] is used to tune the PID controller and their results are compared. The objectives of optimization problem is to minimizing rise time, settling time, ripple and steady state error of the output voltage of the boost converter corresponding to step changes in input voltage and load. The optimization process yields values of  $K_p$ ,  $K_i$ ,  $K_d$  that are optimal for different output voltage ranges. The values of  $K_p$ ,  $K_i$  and  $K_d$  determine the compensator design, performance and stability.

A large proportional gain ( $K_p$ ) results in a large change in the output of the PID compensator for a small change in error. The integral constant ( $K_i$ ) decreases the settling time and helps in eliminating the steady state error and the derivative term ( $K_d$ ) slows the rate of change of the PID compensator output. A PID controller is basically known to be a form of phase lead-lag compensator having a pole located at origin and the other at infinity [18].

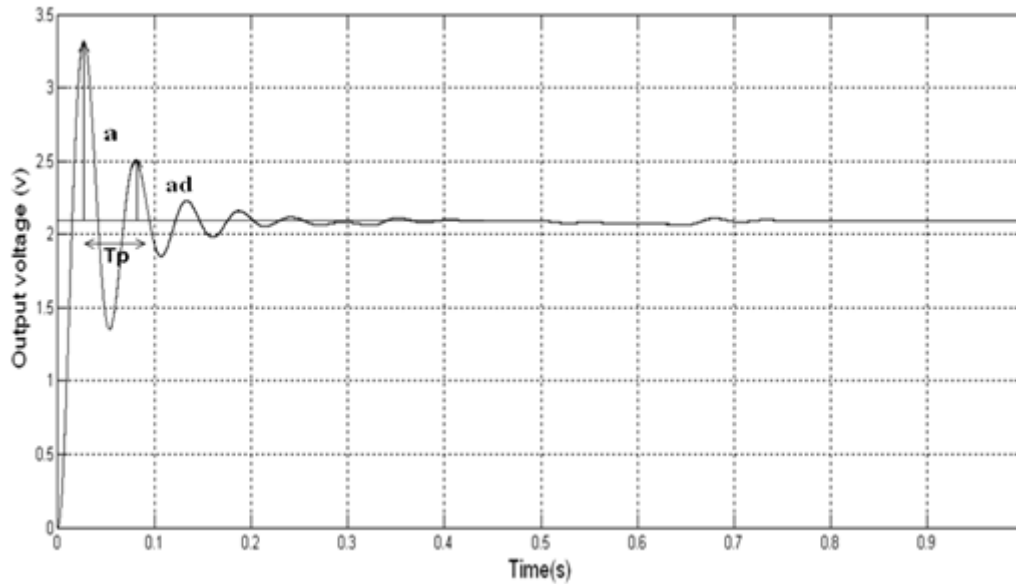


Fig.2.Step response of an under-damped second-order DC boost converter

### 3.1 Transient specified tuned PID controller (T-PID)

The step response of an under-damped second-order design of the DC boost converter is shown in Fig.2 where a, Tp and ad are 3.3,0.35, 0.05 respectively .System with under-damped step response may be approximated by a second-order system with transfer function [19].

$$T(s) = \frac{K\omega_n^2}{s^2 + 2\zeta\omega_n s + \omega_n^2} \quad (6)$$

Where  $\zeta$  is in range of  $0 < \zeta < 1$  for an ideal second-order system with transfer function (6). The step input response with amplitude M is given as in [24],

$$S(t) = JM \left[ 1 - \frac{1}{\sqrt{1-\zeta^2}} e^{-\zeta\omega_n t} \sin(\omega_n \sqrt{1-\zeta^2} t + \phi) \right] \quad (7)$$

toward suitably tuning the controller gain, where  $\phi = \arccos(\zeta)$ . The dc-gain J, the natural frequency  $\omega_n$  and the damping coefficient  $\zeta$  is determined as computed in [19] which are as follows:

$$\zeta = \frac{1}{\sqrt{1 + \left(\frac{2\pi}{\ln d}\right)^2}}, \quad \omega_n = \frac{2\pi}{T_p \sqrt{1-\zeta^2}}$$

The PID controller for the under damped second-order system is presented by rewriting the transfer function (5) as

$$K(s) = \frac{K_p T_d}{s} \left( s^2 + \frac{1}{T_d} s + \frac{1}{T_d T_i} \right) \quad (8)$$

Since the system is of second order, it has a pair of complex poles as  $-\zeta\omega_n \pm j\omega_n \sqrt{1-\zeta^2}$ . The suitable choice for the controller zeros should be such that the numerator polynomial of K(s) and denominator polynomial of T(s) eliminate each other [17]. To obtain this condition,  $T_i$  and  $T_d$  must fulfill the equations as follows,

$$T_d = \frac{1}{2\zeta\omega_n}, \quad \text{and} \quad T_i = \frac{2\zeta}{\omega_n}.$$

The computed values of  $K_p, T_d$  and  $T_i$  for the PID controlled close loop boost converter is given in table 2.

### 3.2 PID gain optimization using PSO algorithm (PSO-PID)

The gain of PID controller used is determined by particle swarm optimization (PSO) algorithm [20]. PSO is a multi-agent parallel search technique where say n flying entities fly through the multidimensional search space as the algorithm progresses through discrete time steps i.e.  $t=0, 1, 2, \dots$ , while keeping the population size m constant. In the standard PSO algorithm, each particle's current position  $X_i(t) = [X_{i,1}(t), X_{i,2}(t), \dots, X_{i,n}(t)]$  and its current velocity  $V_i(t) = [V_{i,1}(t), V_{i,2}(t), \dots, V_{i,n}(t)]$ , where  $i=1, 2, \dots, m$  is considered and accordingly its personal best position  $P_i(t)$  and global best position  $G(t)$  is found with respect to the origin of search space. Here one position is declared better than another if the former gives a lower value of the objective function than the latter. This function is called the fitness function. Each particle's initial position vector component  $X_{i,j}(0)$  is picked randomly from a predetermined search range  $[X_{Lj}, X_{Uj}]$  and its velocity components is initialized by choosing at random from interval  $[-V_{jmax}, V_{jmax}]$ . The initial settings for  $P_i(t)$  and  $G(t)$  is given below

$$P_i(t) = X_i(0), G(0) = X_k(0)$$

such that  $f(X_k(0)) \leq f(X_i(0)) \forall i$ ,

where  $\forall i$ , represent for all values of i [20]. The iterative optimization process for the initialized particle begins and the position and velocities of all the particles are updated by the following recursive equations (9), (10). Given equations are for jth dimension of the position and velocity of the ith particle.

$$\begin{aligned} V_{id}(t+1) &= \omega V_{id}(t) + C_1 \Phi_1 (P_{id}(t) - X_{id}(t)) \\ &+ C_2 \Phi_2 (P_{gd}(t) - X_{id}(t)) \end{aligned} \quad (9)$$

$$X_{id}(t+1) = X_{id}(t) + V_{id}(t+1) \quad (10)$$

Where  $\omega$ : Time-decreasing inertial weight factor designed by Shi and Eberhart [20].  $C_1=2.4, C_2=1.6$ . Two constant

multipliers called self confidence and swarm confidence respectively,  $\Phi_1, \Phi_2$ . Two uniformly distributed random number. The iteration is fixed for certain number of time steps or until the fitness of the best particle at a certain time step is better than a predefined value is obtained. The fittest vector of the final population upon termination of the algorithm is taken as the possible solution of the problem. PSO is more efficient in maintaining the diversity of the swarm, since all the particles use the information related to the most successful particle in order to improve themselves, whereas in Genetic algorithm, the worse solutions are discarded and only the new ones are saved [21]. The PSO parameter used in this application is given in table 1.

**Table1. PSO parameter and value:**

PSO Parameter	Value
Population size	20
$V_{min}$	-3
$V_{max}$	3
$C_1, C_2$	2.4, 1.6
No. of Iterations	40

The parameters obtained from the under-damped second order single boost converter using PID controller with transient performance specification (T-PID) and PSO algorithm is tabulated in table 2.

**Table2. Tuning parameter of PID controller**

PID Parameters	$K_p$	$K_i$	$K_d$
PID controller with transient performance specification	0.092	0.00544	0.05
PSO-PID	114.32	11.56	0

The voltage overshoot is less than a percent, it is tracking 48 volt and settling time is less about 0.02msec and steady state error is zero in case of the PID controller tuned by the PSO algorithm. The simulated result of the single boost converter is shown in Fig3.

As the control of inductor is not considered, therefore we can see that there is overshooting of current is occurring in both the tuning method and as a result there is fluctuation in the output power. As this result determines that tuning of PID controller by PSO algorithm is more suitable for output voltage control of the DC converter, which is implemented in our proposed work in section IV. Here two converters of same kind are connected in parallel for harvesting of distributed renewable energy sources such as photovoltaic module. The objective of paralleling the DC boost converter is to increase the power density and make the DC system more reliable and redundant and the same is tried here to prove in the proposed

work. The performance indices of the PID controller as discussed in [22] is tuned by the above mentioned method and the determined indices value are plotted in table 3. Table 3 shows that the PID controller for the boost converter tuned by PSO method is more effective as compared to the PID controller with transient performance specification (T-PID) method.

**Table3: Performance indices of PID controller**

Performance Index of PID	ISE	IAE	ITAE
PID controller with transient performance specification	24.78	1.008	0.02301
PSO-PID	10.81	0.2854	0.00104

The fitness function of PSO algorithm is defined

The fitness function of PSO algorithm is defined by  $1/ISE, 1/IAE$  and  $1/ITAE$ . The larger values of the fitness function suggest that the performance of the DC converter system response is better with the specified PID parameters in case of PSO applied technique.

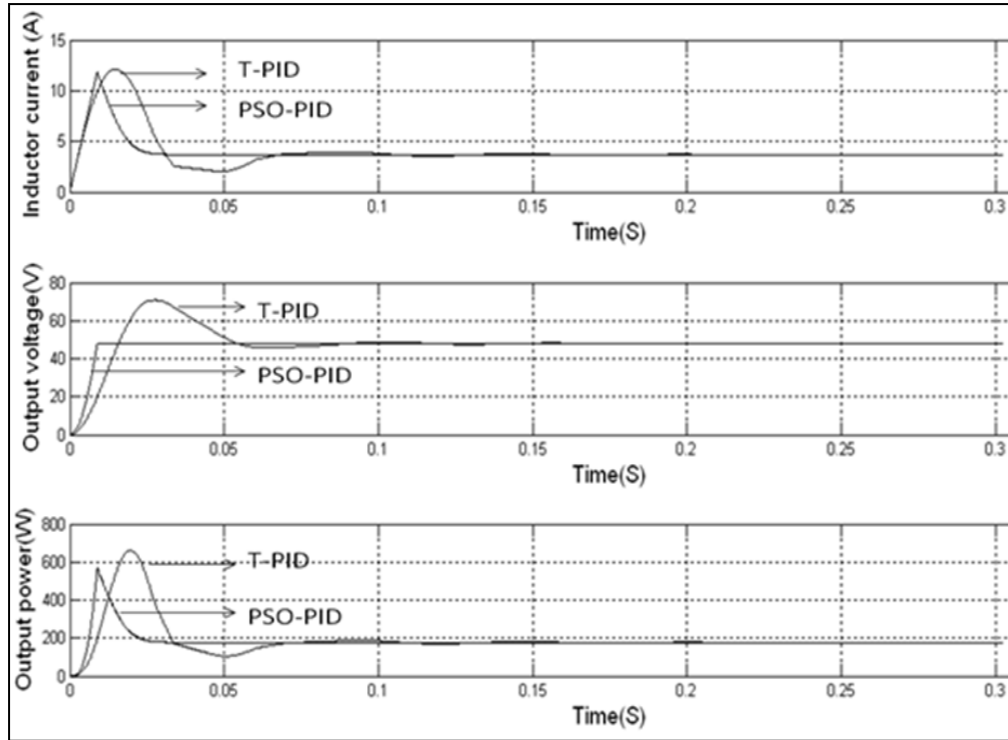
Table 4 shows that the PID controller for the boost converter tuned by PSO method is more effective in terms of settling time and maximum overshoot voltage.

**Table4: Performances evaluation of PID controller**

Parameters	PID controller with transient performance specification	PSO-PID
Settling time	0.16 sec	0.00899 sec
Maximum overshoot voltage	21.28V	0.03V
Steady state error	0.03V	0.03V

#### 4. PROPOSED WORK:

In the proposed type of configuration, each constituent non-isolated converter has its own voltage loop to regulate the output voltage.



**Fig.3. Tuning of PID controller of the single closed loop boost converter by PID controller with transient performance specification and PSO-PID method.**

The need for parallel configuration of converters is to extract maximum power from the existing renewable sources without putting much stress on a particular converter.

The variable output voltage and current of the renewable sources are dependant parameters such as on irradiation and wind velocity etc. The objective of closed loop non-isolated DC converter is to regulate the unregulated output voltage and current of the renewable sources.

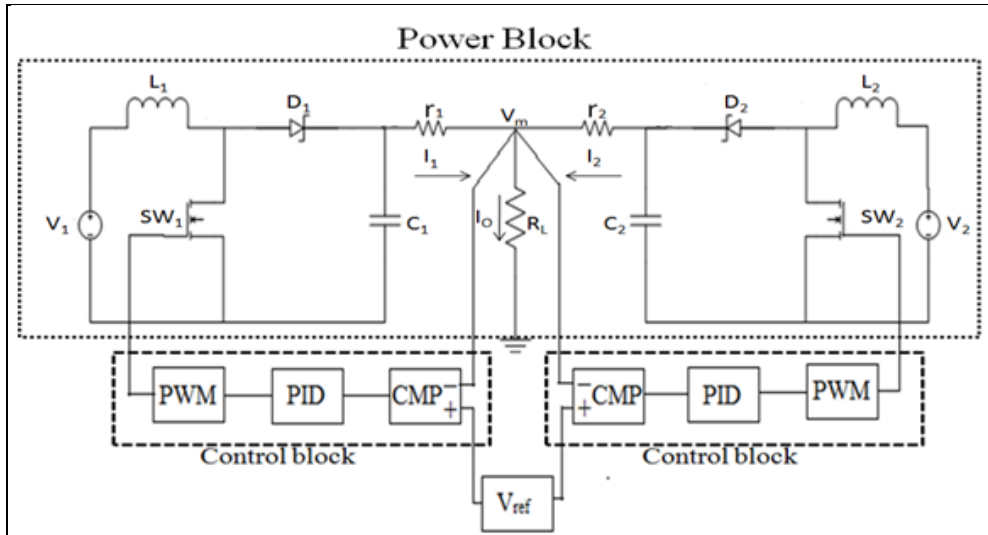
This is achieved using proper current and voltage sharing mechanism. The circuit diagram of the closed loop paralleled converter configuration is shown in Fig 4.

The output impedance of the  $j$ th closed loop converter is given [2] by

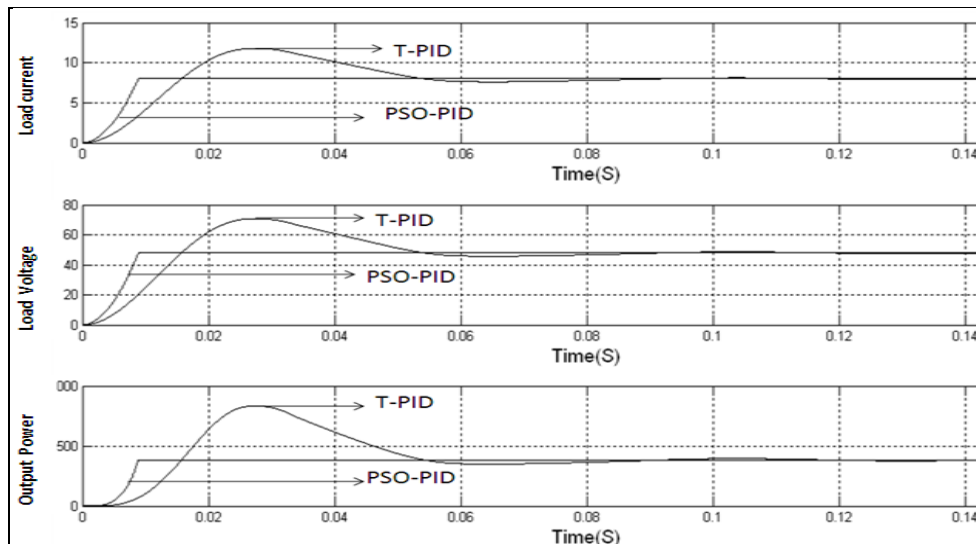
$$Z_{jth} = Z_{CLj} + r_{conj} \quad (11)$$

where  $Z_{CLj}$  is impedance of closed loop converter and  $r_{conj}$  is the connecting wire impedance and the steady-state current-sharing error is expressed as

$$\Delta I = I_1 - I_2 \quad (12)$$



**Fig.4. Paralleled voltage controlled boost converters**



**Fig 5. Tuning method comparison of PID controller between transient performance specification (T-PID) and PSO algorithm method for the paralleled closed loop boost converters.**

This type of configuration can be used with photovoltaic modules as voltage source to regulate the voltage of a bus for DC micro-grid type applications. To accomplish this, here the output voltage is feedback to the comparator where it is compared with a reference voltage and the output is given to PID controller. The PID controller tries to maintain the constant voltage across the load. The gain value of the PID controller is found out using PSO technique and it is one of the best algorithms which provide accurate gain values. The input voltage for left boost converters is 24 volts and for right side converter is 20-24 volt as there is variation in PV module is randomly given to find if there is any difference in output, but in this range no change in output voltage is seen. The output voltage across the load  $6\Omega$  is maintained constant at 48 volts and combined power available at load is 383.44 watt as both DC boost converter are connected in parallel as shown in Fig 5. It is observed in Fig5 that the overshoot, rise time and settling time is significantly less in case of PSO tuned PID controller when compared with the PID controller with

transient performance specification (T-PID) based tuning. The same PSO tuned PID controller for closed loop parallel boost converters is used in [23] and the results obtained are appropriate for paralleling DC/DC converter located at some minimum distances with varying cable resistances.

## 5. CONCLUSION

The objective of enhancing the PID control for unstable processes is proposed in this paper. It is shown in simulation result that there is considerable improvement in the system transient characteristics in terms of peak amplitude, lesser rise time, peak overshoot, settling time and Performance index was also reduced in our proposed application. Results are more refined in modern evolutionary control tuning algorithms than conventional control tuning algorithm. The performance index for various error criterions for the proposed controller using PSO algorithm is proved to be less than the controller tuned by PID controller with transient performance specification (T-PID) method. Therefore, this

method of tuning may be implemented in DC-fed paralleled DC converter systems for low voltage residential applications where tight regulation of voltage is required. The DC sources can be any distributed renewable energy sources like wind, solar photovoltaic and fuel cell etc. The proposed system can be deployed to distributed renewable energy sources in Low voltage (LV) DC micro-grid applications.

## 6. FUTURE WORK

The implementation of evolutionary PSO algorithm in advanced micro-controller for tuning of PID controller in closed loop parallel DC/DC converter may prove to be efficient in regulating the fluctuations of voltages in renewable energy sources. This system used for residential low power applications can prove to be quite useful in energy saving if interfaced with battery bank. The medium to low voltage (48V) DC applications for residential use will be more reliable and efficient in future. These medium to low voltage network can later be reconstructed which may result in formation medium range DC power micro-grids.

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