

VLSI Implementation of IDMA based Multiple Carrier Code Division Multiple Access for 4G Technologies

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ABSTRACT

The aim of this paper is to design a transceiver that achieves a data rate of minimum 4Gbps equivalent to the 4G standard of IEEE. Multiple Carrier Code Division Multiple Access (MC-CDMA) is the combination of code division multiple access (CDMA) and orthogonal Frequency Division Multiplexing (OFDM), therefore the advantages of both schemes can be achieved. In MC-CDMA scheme each data symbol is spreaded in frequency domain and transmitted on different subcarrier which eliminates frequency selective fading and show significant improvement in bit error rate performance. The existing system uses 1-dimensional interleaver for storing data from spreader. It has insufficient memory to interleave incoming data that provides bottleneck that reduces total system efficiency. To overcome this problem, a method called reconfigurable interleaver division multiple access (IDMA) based MCCDMA is proposed. It provides array type of interleaver that has multiple rows and multiple columns, so it can adapt to changes array based on incoming data. Thus it has efficiency to handle bottleneck by processing multiple data. Thus the throughput of the system will be increased.

Keywords

CDMA; IDMA; MC-CDMA; OFDM

1. INTRODUCTION

MC-CDMA is a promising scheme because of an efficient fast Fourier transform (FFT) technique and multiple access technique. It spreads the multiuser data signal in narrow band spectrum which can reduce effect of multi path frequency-selective fading channel and prevents inter symbol interference (ISI)[4]. MC-CDMA is a technique for overcoming the relative capacity limitation of direct-sequence CDMA (DS-SS) and now developed for high-data-rate wireless communication standards[11]. Multiple - input multiple-output (MIMO) array antenna schemes are an interesting idea to fulfill the requirements on bandwidth efficiency and reduce effect of fading channel which these techniques can usefully move the diversity from the base stations (BS) to the mobile units[5].

2. FREQUENCY INTERLEAVING

Interleaver simply generates permuted sequence of input data elements. Associated with any interleaver is a de-interleaver that restores the original input data sequence. Interleavers are generally used after channel coding so as to rearrange the ordering of coded data. The channel coding techniques are designed to detect and correct a specific number of random

errors depending on the error detecting and correcting capability of the given code. The performances of random error correcting codes are worst in fading channel because they cannot handle long burst of error. Moreover, burst error correcting codes are computationally expensive to correct long burst of error. Hence, in order to make it possible to use simple random error correcting codes, in typical wireless communication system, interleaver is employed after channel coding. The purpose to introduce interleaver after channel coding is to improve the performance of channel coding.

3. PROBLEM STATEMENT AND CONTRIBUTION

In recent years a new multiple access technique, where the users are separated through their unique interleaving patterns, has generated a large interest in the research community. The technique, referred to as interleave-division multiple access (IDMA), has been shown to mitigate multiple access interference while simultaneously achieving a high spectral efficiency [6]. The performance of code-division multiple-access (CDMA) systems is mainly limited by multiple access interference (MAI) and Inter Symbol Interference (ISI).

A conventional random waveform CDMA (RW-CDMA) system (such as IS-95) involves separate coding and spreading operations. Theoretical analysis shows that the optimal multiple access channel (MAC) capacity is achievable

when the entire bandwidth expansion is devoted to coding. This suggests combining coding and spreading using low rate codes to maximize the coding gain. In this case, interleavers can be employed to distinguish signals from different users and low receiver complexity. This scheme relies on interleaving as the only means to distinguish the signals from different users, and hence it has been called interleave division multiple-access (IDMA). IDMA inherits many advantages from CDMA, in particular, diversity against fading and mitigation of the worst-case other cell user interference problem. Furthermore, it allows a very simple chip-by-chip iterative Multi user Detection (MUD) strategy[1]. The normalized MUD cost (per user) is independent of the number of users.

In this paper throughput performance of MCCDMA over IDMA is investigated. A comparison is made between 1-D interleaver in MCCDMA and MCCDMA over IDMA among the different number of users. Simulation results shows that the throughput increases in IDMA based MCCDMA technique with number of users.

The rest of the paper is organized as follows: Section4 describes the transmitter and receiver model for MC-CDMA system. Section5 focuses on interleaver design. Performance results are shown in section7 and conclusion is given in section8.

4. SYSTEM MODEL

Fig-1 shows the simulation model of MC-CDMA transmitter. Randomly generated user data are randomized and output of randomizer is fed to forward error correction (FEC) block where randomized data are coded by Reed

Solomon and convolutional encoder. FEC code has high gain and adopted in WCDMA and WiMAX standards[10]. Viterbi algorithm is used for decoding the convolutional coding[9]. Output of FEC code is interleaved and modulated[12]. For modulation 16-QAM is used. Modulated output is spreaded by Walsh - Hadamard spreading code in frequency domain then transmitted simultaneously on N_c parallel subcarriers[8]. Spreading will convert narrowband signal to wideband signal to suppress the self-interference by multipath effect. Various types of spreading codes, like Walsh- Hadamard (W-H) code, Pseudo Noise (PN) spreading code,

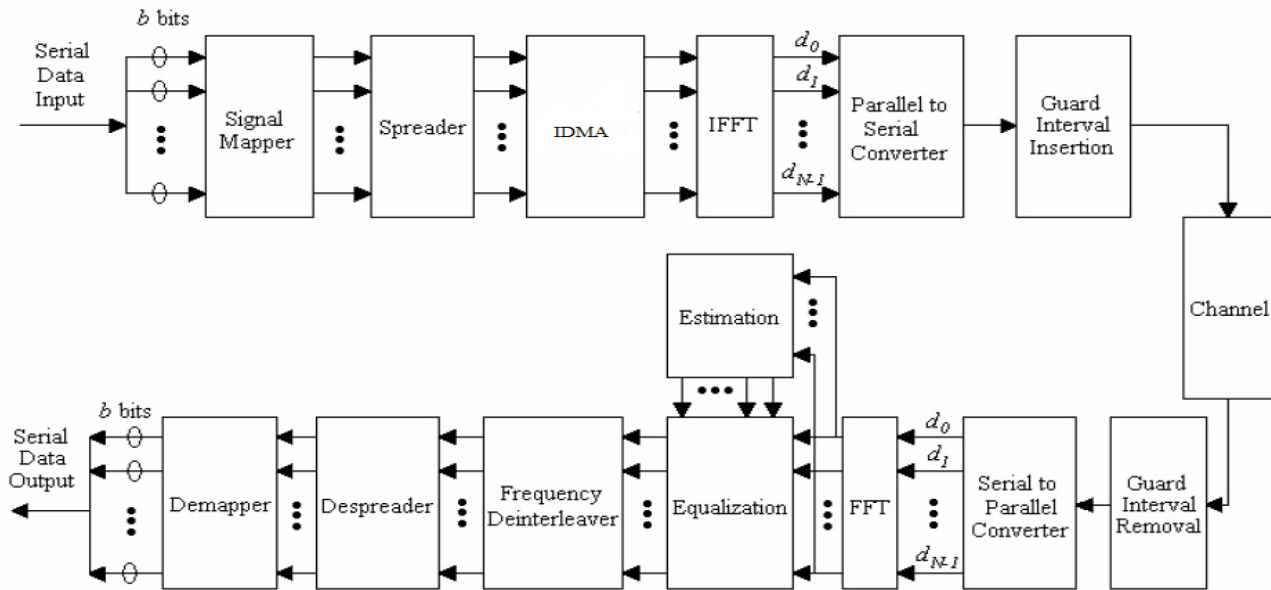


Fig 1: IDMA based MCCDMA model

Gold code, Golay code are used to spread the user data. These codes can be distinguished each other from their correlation properties, orthogonality and peak average to power ratio (PAPR). Since W-H code is orthogonal code its performance is good among the above spreading code. To minimize the MAI spreading code should be orthogonal. In synchronous downlink for MAI reduction W-H code can be used. In uplink PN code is used because due to distortion spreading codes orthogonality gets lost. Also PN sequences are employed in FHSS system[2]. In view of correlation property Gold code is suitable option. For asynchronous transmission W-H code and Gold code are used in WCDMA standard. Signals become more sensitive to non linearities of transmitter for high peak average to power ratio. In uplink PAPR can be reduced by Golay code. These parallel subcarriers are orthogonal to each other and can be generated by using Inverse Fast Fourier transform (IFFT). After this cyclic prefix is used as a guard interval to minimize the effect of inter carrier interference (ICI). Now parallel to serial converter (P/S) converts parallel data into serial data stream and transmit over channel. At receiver first convert serial data into parallel by serial to parallel converter (S/P) then remove the cyclic prefix and take the Fast Fourier Transform (FFT) of received signal. After that despread and demodulate the users signal. The output of demodulator passes through the channel decoder. The decoded output is derandomized and obtained the users data.

5. INTERLEAVER

The input data sequence b_n of user-n is encoded based on a low-rate code C , generating a coded sequence $c_k = [c_k(1), \dots, c_k(j), \dots, c_k(J)]^T$, where J is the frame length. The elements in c_k are referred to as coded bits. Then c_k is permuted by an interleaver π_k , producing $x_k = [x_k(1), \dots, x_k(j), \dots, x_k(J)]^T$. The elements in x_k "chips". Users are solely distinguished by their interleavers, hence the name interleaved-division multiple-access(IDMA). The key principle of IDMA is that the interleavers $\{\pi_k\}$ should be different for different users. It is assumed that the interleavers are generated independently and randomly. These interleavers disperse the coded sequences so that the adjacent chips are approximately uncorrelated, which facilitates the simple chip-by-chip detection scheme.

$$D_R = F_R * N_T \quad (1)$$

Where D_R is the data rate of the active user, F is the frequency of the transmitter or receiver, N_T is the number of bits transmitted.

6. CHANNEL ESTIMATION USING THE FFT METHOD

A Rayleigh fading compensation technique using the FFT method was originally proposed for QAM signals[3]. The same technique is applied for a MC-CDMA signal. Pilot chips are inserted before the IFFT block at the transmitter or can be said to be in the frequency domain. The least squares

estimate of the pilots is obtained in the receiver by dividing those corrupted pilots with the known pilot symbols. To obtain the channel coefficients for the data symbols interpolation is performed in the frequency domain. In conventional CDMA, only one carrier signal is modulated and thus if the signal undergoes any multipath fading, then the total signal may be distorted. Multiple carrier modulation technique like MC-CDMA allows the receiver to receive information correctly even when some of subcarriers are affected by multipath fading.

7. RESULTS & DISCUSSIONS

Each block of MC-CDMA transmitter and receiver is individually coded. Reed Solomen and convolution code is used as a channel coding. W-H code has been chosen for spreading the user data, cyclic prefix is taken as $\frac{1}{4}$, number of subcarrier N_c is kept equal to the spreading code length. The transmitter data rate for 32 data bits at the frequency of 130.4MHz is 4.17Gbps. The receiver data rate at the frequency of 194.14MHz is 6.21Gbps. The total data rate is the average of transmitter and receiver data rates. For an ordinary interleaver the throughput achieved is 1.5Gbps and with IDMA the throughput achieved will be minimum of 4Gbps. For an ordinary interleaver 325MHz of frequency is achieved whereas in reconfigurable interleaver executes at speed of 1684MHz frequency.

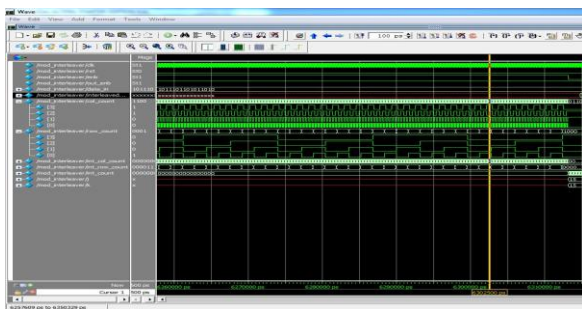


Fig 2: Simulation results of IDMA

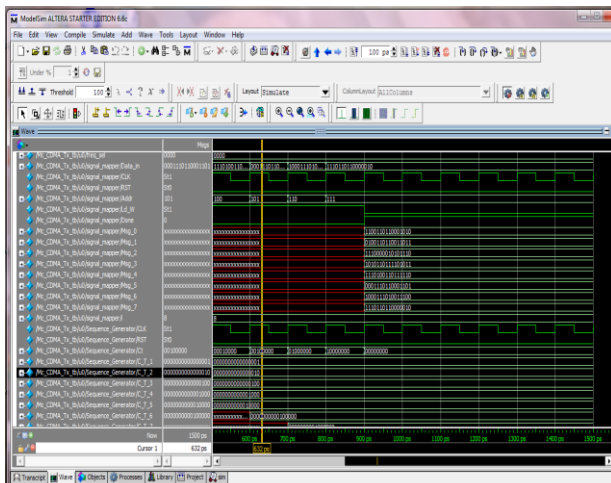


Fig 3: IDMA-MCCDMA Transmitter module

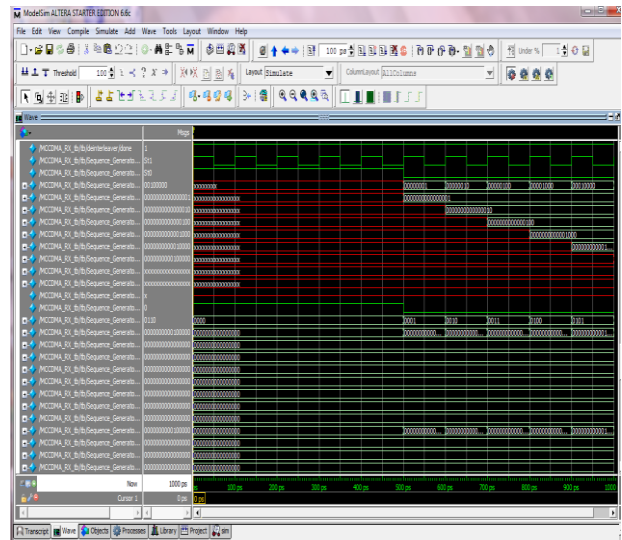


Fig 4: IDMA-MCCDMA Receiver module

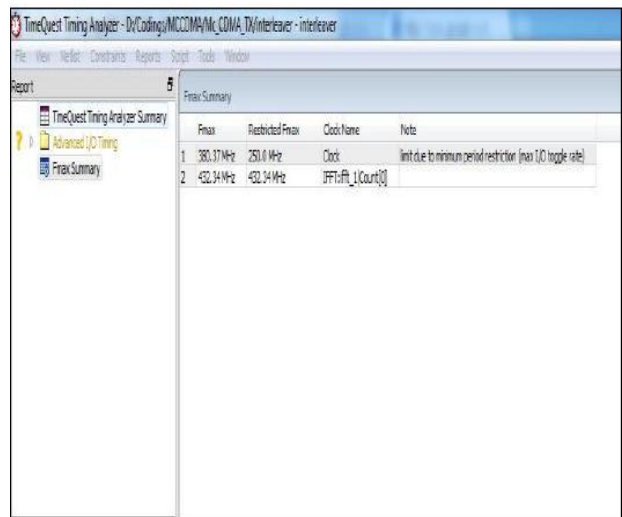


Fig 5: Throughput result of 1-Dimensional interleaver

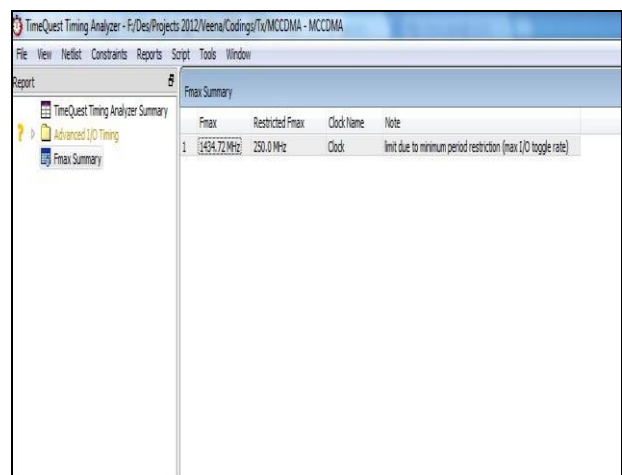


Fig 6: Throughput result of IDMA

Fmax Summary			
Fmax	Restricted Fmax	Clock Name	Note
1 520.83 MHz/ 250.0 MHz		Clock	limit due to minimum period restriction (max I/O toggle rate)
2 553.71 MHz/ 553.71 MHz		Delay_insertion_guard_interval_insertion_Count[0]	

This panel reports FMAX for every clock in the design, regardless of the user-specified clock periods. FMAX is only computed for paths where the source and destination registers or ports are driven by the same clock. Paths of different clocks, including generated clocks, are ignored. For paths between a clock and its inversion, FMAX is computed as if the rising and falling edges are scaled along with FMAX, such that the duty cycle (in terms of a percentage) is maintained. Altera recommends that you always use clock constraints and other slack reports for sign-off analysis.

Fig 7: Transmitter Throughput

Fmax Summary			
Fmax	Restricted Fmax	Clock Name	Note
1 236.57 MHz/ 236.57 MHz		Clock	
2 399.2 MHz/ 399.2 MHz		CPSK_Sequence_Generator_Count[0]	

This panel reports FMAX for every clock in the design, regardless of the user-specified clock periods. FMAX is only computed for paths where the source and destination registers or ports are driven by the same clock. Paths of different clocks, including generated clocks, are ignored. For paths between a clock and its inversion, FMAX is computed as if the rising and falling edges are scaled along with FMAX, such that the duty cycle (in terms of a percentage) is maintained. Altera recommends that you always use clock constraints and other slack reports for sign-off analysis.

Fig 8: Receiver throughput

Table 1. Comparison between 1 – D based MC –CDMA & IDMA based MC - CDMA

Syatem Name	Interleaver capacity	Transmitter Capacity
1 – D interleaver based MC - CDMA	6.085 Gbps	2.5 Gbps
IDMA based MC - CDMA	22.955 Gbps	8.33 Gbps

8. CONCLUSIONS

In this paper throughput performance of MCCDMA over IDMA is described. Improved data rate performance will increase the capacity of the system. The simulation result shows that the throughput is high enough to support 4G technologies. When Compare with the 1D interleaver in MC-CDMA, the IDMA based MCCDMA produce high data rate for the same number of data. In further the simulation results for IDMA based MCCDMA and comparisons between 1-D

interleaver based MCCDMA and IDMA based MCCDMA to were done. In future work channel equalization techniques can be obtained for receiver in this method.

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