

An Efficient Monarchic Reconfiguration Protocol with Deadlock Freedom on Interconnection Networks

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ABSTRACT

Monarchic Reconfiguration protocol (MRP) is the self-governing, independent and autonomous system to resolve the deadlocking problem during reconfiguration in interconnection networks. Deadlock is the cyclic dependency between the old and new routing functions. Our aim is to protect the routing path with deadlocking freedom and improve the performance drastically. Our proposed method will increase the availability and dependability of the network and reduce 100 % packet drops ratio with deadlock freedom.

1. INTERCONNECTION NETWORKS

High-performance interconnection networks comprise the communication backbone in digital systems at several system levels. At the higher system levels, local-area networks (LANs) are used in clusters of PCs, networks of workstations and other distributed processing systems which serve as cost/performance effective alternatives to tightly-coupled massively parallel processing systems. System-area networks (SANs) are used for interconnecting processors, memories, and I/O devices in systems with the primary goal of increasing reliability in the presence of link/router failures. Storage-area networks (STANs)[1]are used to increase performance and reliability of large disk arrays by offering access to stored data by processors through multiple paths, thus providing continued service in the presence of processor failure. Internet protocol router fabric (IPRF) networks are used within IP routers to handle IP traffic at high (multi gigabit) sustained line rates. Server I/O(SIO) and inter processor communication (IPC) [2,3] networks are used to overcome many of the scalability limitations of multi chip bus-based systems, allowing high-speed interconnections between memory controllers and I/O devices, direct access to disk from LAN adapters, and concurrent communication between processors, memories and I/O devices in multiprocessors. Likewise, at lower levels, networks-on-chip (NOCs) [4,5,6]are used to overcome many of the performance limitations of bus-based systems at the chip level. Parallel computing and communication systems built from the above networks require high-performance communication services [7] with high reliability, availability and dependability - collectively, high robustness. The performance [8] of the interconnection network is measured, in part, by packet delivery time[9] from source to destination (i.e., *latency*) and by the number of packets delivered per unit time (i.e., *throughput*). In essence, a high-performance network allows the maximum number of packets to make forward progress to their destinations in minimal time, preferably along shortest paths [10] to preserve network bandwidth. Likewise, the reliability, availability and dependability of a network equally impact the overall goodness (*quality of a system*). These attributes are measured,

in part, by the network's ability to remain up and running at near normal levels even when events occur which change its configuration, possibly due to changes in users' needs and/or system state. Such reconfiguration events may include, for example, hot-swapping of components, failure or addition of links/nodes, activation or deactivation of hosts/routers, etc., in a LAN [24] environment. Since network resources are finite and, ultimately, are contended for, structural hazards on those resources are inevitable which delay or prevent packet transmission in the network. This occurs even in networks that feature advanced router architectures. Such hazards cause packets to block which, eventually, can lead to network congestion and, possibly, *deadlock*. One of the more critical problems to be addressed in order to achieve high network performance and robustness is that of efficiently handling deadlock anomalies. The rest of the paper organized as follows. Section 2 provides the idea description about the proposed system. Section 3 describes the related work about the system. Section 4 deals with limitations of reconfiguration. Section 5 provides network model and assumption related to the proposed system and also this section introduces the concept of Monarchic Reconfiguration Protocol (MRP).

1.1. Network and Router Model

Direct networks consist of a set of nodes interconnected by point-to-point links or channels. No restriction is imposed on the topology of the interconnection network. Each node has a router. We assume that the switch is a crossbar, therefore allowing multiple packets [11] to traverse a node simultaneously without interference. The routing and arbitration unit configures the switch [12], determining the output channel for each packet as a function of the destination node, the current node, and the output channel status. The routing and arbitration unit can only process one packet header at a time. If there is contention for this unit, access is round-robin[13]. When a packet gets the routing and arbitration unit but cannot be routed because all the valid output channels are busy, it waits in the corresponding input buffer until its next turn. By doing so, the packet gets the first valid channel that becomes available when it is routed again. This strategy achieves a higher routing flexibility than strategies in which blocked packets wait on a single predetermined channel. Physical channels [14] are bidirectional full duplex. Physical channels may be split into virtual channels. Virtual channels are assigned the physical channel cyclically, only if they are ready to transfer a flit [21](demand-slotted round-robin). Fig 1 shows the basic router model

The interconnection network I is modeled by using a strongly connected directed graph with multiple arcs, $I = G(N, C)$. The

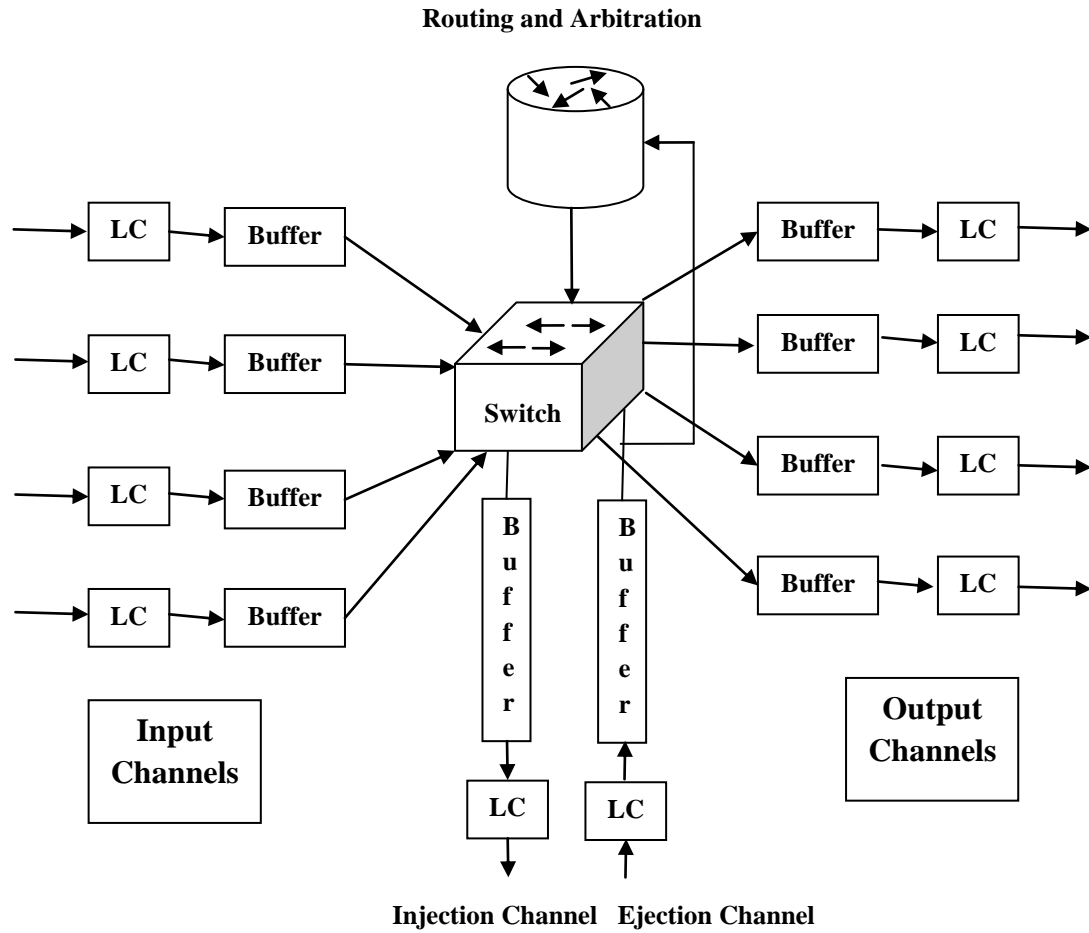


Fig. 1 A basic router Model

vertices of the graph N represent the set of processing nodes. The arcs of the graph C represent the set of communication channels. More than a single channel is allowed to connect a given pair of nodes. Bidirectional channels [15] are considered as two unidirectional channels. We will refer to a channel and its associated edge buffer indistinctly. The source and destination nodes of a channel c_i are denoted s_i and d_i , respectively. A routing algorithm is modeled by means of two functions: routing and selection.

The *routing function* [16] supplies a set of output channels based on the current and destination nodes. A selection from this set is made by the *selection function* [17] based on the status of output channels at the current node. This selection is performed in such a way that a free channel (if any) is supplied. If all the output channels are busy, the packet will be routed again until it is able to reserve a channel, thus getting the first channel that becomes available. As we will see, the routing function determines whether the routing algorithm is deadlock-free or not. The selection function only affects performance.

2. MOTIVATION

Communication sub-systems of interconnection networks are made up of advanced switches and routers. Based on the routing method, it is classified as distributed [22], and source routing models [23]. The sub-system are centrally monitored by autonomous system called subnet manger [20] (distributed routing) and fabric manager or mapper [19] (source routing).

During the reconfiguration process, the managers collect all information (knowledge) of the network based on change assimilation process and prepares the new path for packet forward. The ancient method to maintain deadlock free path preparation is static reconfiguration. Here process happened in three phases. During the first phase, injection of packets in to the network is stopped. In second phase, the network has to wait until a packet in exiting network has to drain. And in third phase packets are injected in to the network with new routing paths. In this method we can achieve cent percent deadlock freedom but network down times are high. The recent popular methods are dynamic reconfiguration. It allows both old and new routing packets simultaneously but we need a separate mechanism to split old and new packets inside the network. In this paper, we propose the new protocol in dynamic manner, to reduce the reconfiguration occurrences and path establishment with deadlock freedom. In this strategy, a group is formed around the failure link or node and elects a node as leader to get the planning algorithm from gateway. Monarchic Reconfiguration Protocol (MRP) will provide the right path with deadlock freedom.

3. RELATED WORK

The most recent method proposes that uses a close graph-based reconfiguration method, it works based on up*/Down*[30] routing method. Any topology change occurs during reconfiguration the centralized autonomous system[16] calculates new path by Fully Explicit Algorithm[33] for distributed network and dijkstra based algorithm [18] in

source routing networks. Next recent approach is epoch marking system, it guarantees that only packets potentially leading to a deadlock will be removed in saturation condition. It is based on regressive deadlock recoveries and used in distributed networks. Overlapping Static Reconfiguration (OSR) [31] is a static method introduce a TOKEN (a special packet) in-between the old and new routing functions. Deadlocks are avoided by ensuring that each link first transmits packets that belong to the old routing function, then the token, and finally packets that belong to the new one. This method tested in both source and distributed routing. Another popular method, Double Scheme (DS) [28] requires additional resources in order to work with two disjoint sets of virtual channels to separate packets routed according to the old routing function from packets routed according to the new. NetRec [29] and LORE [32] have been specially designed for rerouting messages around a faulty node. The NetRec scheme requires every switch to maintain information about switches some number of hops away. LORE relies on the existence of virtual channels to reroute the packets headed toward the fault. The implementation of these techniques is not feasible in networks with source routing. The reason is that it is not possible for an intermediate switch to make the decision to route a packet through a different path. The Partial Progressive Reconfiguration (PPR)[27] and Skyline [25] methods to repair an uncorrected up* / down* graph and compute a new graph were based on spanning tree. PPR transforms the invalid up*/down* graph into valid sub regions that together form a valid up*/down* graph. Skyline is a technique to identify the region of the network that must be reconfigured after the change. PPR and Skyline were designed for networks that use distributed routing, in which forwarding decisions are taken locally by each switch in the packet path. Finally the very ancient method of reconfiguration is static [26] one. When a static reconfiguration scheme is used deadlock situations are not an issue since packets that are routed according to the old routing function and packets that are routed according to the new routing function are not simultaneously present in the network. This method is not an efficient one because network down times are high. Virtual channel [21] is also another static method but it requires additional resources.

4. LIMITATIONS ON RECONFIGURATION

Even though many solutions for interconnection network to recover from link failures have been proposed, they still have several limitations as follows. First, resource-allocation algorithms can provide (theoretical) guidelines for initial network resource planning. However, even though their approach provides a comprehensive and optimal network configuration plan, they often require “global” configuration changes, which are undesirable in case of frequent local link failures. Next, a *greedy* channel-assignment algorithm can reduce the requirement of network changes by changing settings of only the faulty link(s). However, this greedy change might not be able to realize full improvements, which

can only be achieved by considering configurations of neighboring mesh routers in addition to the faulty link(s). Third, fault-tolerant routing protocols, such as local rerouting or multipath routing can be adopted to use network-level path diversity for avoiding the faulty links. However, they rely on detour paths or redundant transmissions, which may require more network resources than link-level network reconfiguration. To overcome all, MRP protocol includes a monitoring protocol that enables to perform real-time failure recovery in conjunction with the planning algorithm. The accurate link-quality information from the monitoring protocol is used to identify network changes that satisfy applications’ new QoS demands or that avoid propagation of QoS failures to neighboring links. First MRP’s planning algorithm effectively identifies reconfiguration plans that maximally satisfy the applications’ QoS demands, accommodating twice more flows than static assignment. Second, MRP avoids the ripple effect via QoS-aware reconfiguration planning, unlike the greedy approach. Third, MRP’s local reconfiguration improves network throughput and channel efficiency over the local rerouting scheme.

5. PROPOSED NETWORK MODEL AND ASSUMPTIONS

The proposed network model is assumed to consist of mesh nodes, links, and one control gateway. Each mesh node and links assignments are initially made by using global channel/link assignment algorithms. During its operation, each mesh node periodically sends its local channel usage and the quality information for all outgoing links via management messages to the control gateway. Based on this information, the gateway controls the admission of requests for data flows. Then implement the MRP to improve network performance during reconfiguration.

5.1 Monarchic Reconfiguration Protocol (MRP)

We first present the overview of MRP. Then, we detail the MRP’s reconfiguration algorithms. Finally, we discuss the functionality of MRP protocol.

5.1.1 Overview

MRP is a distributed system that is easily deployable in interconnection networks. Running in every mesh node, MRP supports self-reconfigurability via the following distinct features.

- *Localized reconfiguration*: MRP generates reconfiguration plans that allow for changes of network configurations only in the vicinity where link failures occurred while retaining configurations in areas remote from failure locations.
- *QoS-aware planning*: MRP effectively identifies QoS-satisfiable reconfiguration plans by: 1) estimating the QoS

Network Layer

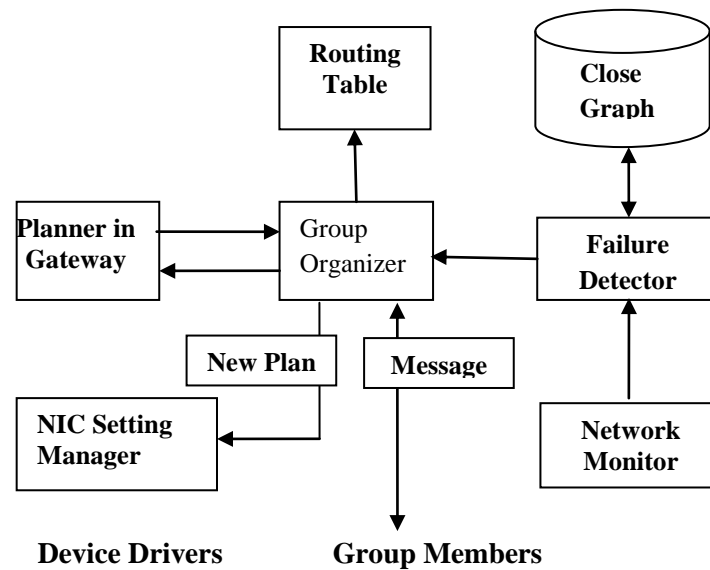


Fig. 2 Block diagram of MRP

satisfiability of generated reconfiguration plans; and 2) deriving their expected benefits in channel utilization. The main modules of MRP protocol is shown in fig. 2.

5.1.2 Algorithm of MRP Protocol

Algorithm : MRP Operation at mesh node

Monitoring

- 1: **for every** link **do**
- 2: measure link-cost using passive monitoring;
- 3: **end for**
- 4: send monitoring results to a gateway;

Failure detection and group formation

- 5: **if** link violates link requirements **then**
- 6: request a group formation on channel of link;
- 7: **end if**
- 8: participate in a leader election if a request is received;

Planning

- 9: **if** node is elected as a leader **then**
- 10: send a planning request message to a gateway;
- 11: **else if** node is a gateway **then**
12. synchronize requests from reconfiguration groups
- 13: generate a reconfiguration plan;
- 14: send a reconfiguration plan to a leader;
- 15: **end if**

Reconfiguration

- 16: **if** includes changes of node **then**
- 17: apply the changes to links;
- 18: **end if**
- 19: relay to neighboring members, if any

5.1.3 Functionality of MRP

• *Autonomous reconfiguration via link-quality monitoring:*

MRP accurately monitors the quality of links of each node in a distributed manner. Furthermore, based on the measurements and given links, QoS constraints, MRP detects local link failures and autonomously initiates network reconfiguration.

• *Cross-layer interaction* MRP actively interacts across the network and link layers for planning.

This interaction enables MRP to include a rerouting for reconfiguration planning in addition to link-layer reconfiguration. MRP can also maintain connectivity during recovery period with the help of a routing protocol.

Algorithm describes the operation of MRP as shown in fig 3. First, MRP in every mesh node monitors the quality of its outgoing links at every (e.g., 10 s) and reports the results to a gateway via a management message.

Second, once it detects a link failure(s), MRP in the detector node(s) triggers the formation of a group among local mesh routers that use a faulty channel, and one of the group members is elected as a leader using the well-known bully algorithm for coordinating the reconfiguration.

Third, the leader node sends a planning-request message to a gateway. Then, the gateway synchronizes the planning requests—if there are multiple requests—and generates a reconfiguration plan for the request. Fourth, the gateway sends a reconfiguration plan to the leader node and the group members.

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