

A Novel based Cooperation and Joint Source- Channel Transmission in Wireless Networks using LDPC

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ABSTRACT

In this paper, A Novel based Cooperation and joint source channel Transmission in wireless networks in this paper Low density parity check (LDPC) codes is linear different number of the forward error correcting codes, method of transmitting the number of messages over noisy transmission of a channel . Low density parity check (LDPC) codes are mainly used to minimize the outage probability. We summarize the methods based on the Read Solomon (RS) codes and the Progressive Edge Growth (PEG) algorithm in selection. The first,we select a profile that describes that desire the desired number of columns of each weight and the desired number of rows of each weight. The symbols produced by the information source are given to the source encoder. These symbols cannot be transmitted directly. They are first concerted into digital form .The source encoder assign codeword's to the symbols for each discrete symbol there is an unique codeword. The transmission errors basically are of two types namely the random errors and burst errors. The errors that occurs in a purely random manner are known as random errprs.Where as the errors which occur in the form of bunches and hence which the not independent are known as the burst errors. Codes like binary BCH codes are useful in dealing with the random error.Because of impulse noise, more than one corresponding data bits change their state simultaneously. The error introduced in the received word is called as burst error. The channel encoder and decoder is after converting the message (or) information signal in the form of binary sequence by source encoder. The signal is to transmit through channel. The communication channel adds some noise and interference to the signal being transmitted. Hence errors are introduced in the binary sequence received at the receiver end.

Index Terms

Delay limited capacity, Outage capacity, power control fading channel, Quasi stationary sources, outage distortion of the channel, source coding and channel coding, rate of the channel and power adaptation, Low density parity check codes (LDPC).

1. INTRODUCTION

Low Density Parity Check (LDPC) codes are mainly used to the forward error correction codes. As the name suggest, the LDPC codes are block codes defined as parity check matrix that is sparge.They are first time proposed in the year of 1962 by the great scientist Gallager.

The Gallager is to proposed in his Ph.D thesis in the year of 1962 at Massachusetts Institute of Technology (MIT).At that time of their increasing the potential of the LDPC codes to performance and simulation in the vacuum tubes, these tubes is to transmit the digital signal. In 1995they were rediscovered by MacKay and Neal sparking a flurry of farther research.

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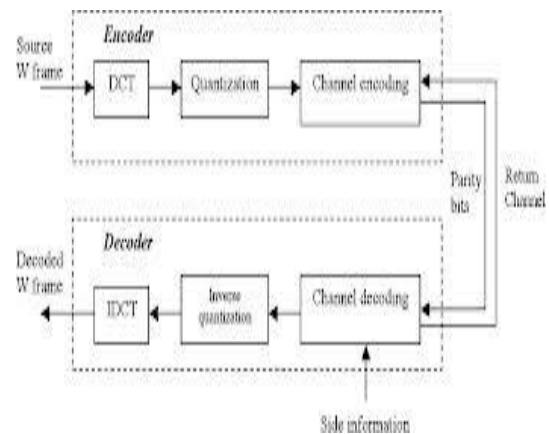


Fig 1 Low density parity check (LDPC) codes

1.1 Source Channel Coding Techniques

The source coding theorem states that for a discrete memory less source X, with entropy of the symbol $H(X)$, the average code word length L per symbol is bounded as

$$L \geq H(X)$$

Low density parity check (LDPC) codes are mainly used to the forward error correction codes. Low density parity check (LDPC) codes are used to minimize outage probability.

The Shannon Hartley low underscores the fundamental rate of bandwidth and signal to noise ratio(S/N) in communication. It is also shows that we can exchange increased bandwidth for

decreased signal power for a system with given capacity C. The low density parity check (LDPC) codes were discovered by the great scientist Gallager in the year of 1960's.

Coding is the most important application of information theory. When digital data from an information source is transmitted, it experiences various types of noise, distortions and interference in the channel before reaching the destination. Due to this the received data is erroneously received. Hence, there is a need for error control, which involves error detecting and then error correction.

To achieve this coding of the information signal is required. The main purpose of coding is to improve the efficiency of the digital communication system, in the sense, improve the bit error rate (BER) at the output of the receiver.

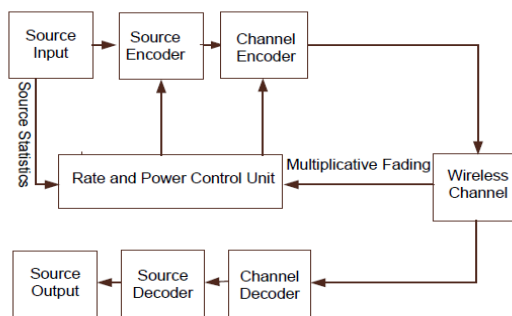


Fig 2 Block diagram of the system

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1.2 LDPC Encoding

In LDPC codes, it is noted that the Generating matrix [G] and Parity check matrix is [H]. The two matrices can be used by performance of Gauss-Jordan elimination on the Parity check Matrix [H] in the form of

$$H = [A, I_{n-k}]$$

The above equation A is a binary matrix of order (n-1) and identity matrix I of the order of (n-k). The Generator matrix G is represented in the form

$$G = [I_k, A^T]$$

The above equation A is a binary matrix of order k and I is an identity matrix of order k. In LDPC codes, the length of rate is $1/2$.

$$H = \begin{bmatrix} 1 & 1 & 0 & 1 & 1 & 0 & 0 & 1 & 0 & 0 \\ 0 & 1 & 1 & 0 & 1 & 1 & 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 1 & 0 & 1 & 0 & 1 \\ 1 & 1 & 0 & 0 & 1 & 1 & 0 & 1 & 0 & 1 \\ 0 & 0 & 1 & 0 & 1 & 0 & 1 & 0 & 1 & 1 \end{bmatrix}$$

First, the above parity check matrix [H] is put into row echelon form (i.e. so that in any two successive rows of the Parity matrix H that do not consist entirely of zeros, the leading 1's two sum of the 5th and 4th rows gives Hr in row

$$H_r = \begin{bmatrix} 1 & 1 & 0 & 1 & 1 & 0 & 0 & 1 & 0 & 0 \\ 0 & 1 & 1 & 0 & 1 & 1 & 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 1 & 0 & 1 & 0 & 1 \\ 0 & 0 & 0 & 1 & 1 & 1 & 1 & 1 & 1 & 0 \\ 0 & 0 & 0 & 0 & 1 & 1 & 1 & 0 & 0 & 1 \end{bmatrix}$$

The next parity check matrix [H] in the parity check matrix is put in the rows echelon form. The 1st column of the parity check matrix is already corrected and entry in to the 2nd column of the parity check matrix above the diagonal is to be removed in the 2nd column and replacing the 1st row in the above matrix.

$$H_{rr} = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 1 & 1 & 1 & 0 \\ 0 & 1 & 0 & 0 & 0 & 1 & 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 1 & 0 & 1 & 0 & 1 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 1 & 1 \\ 0 & 0 & 0 & 0 & 1 & 1 & 1 & 0 & 0 & 1 \end{bmatrix}$$

The above parity check matrix in the form of Hstd,

$$H_{std} = \begin{bmatrix} 0 & 1 & 1 & 1 & 0 & 1 & 0 & 0 & 0 & 0 \\ 1 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 1 & 0 & 1 & 0 & 1 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 1 & 1 & 0 & 0 & 0 & 1 & 0 \\ 1 & 1 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

In the final stage, column permutations have been used so the codeword of the matrix Hstd will be the same code words corresponding in the parity check matrix. The problem is solved by keeping track of the column and creating the matrix Hstd. The matrix Hstd is written as the

$$\Pi = [6 \ 7 \ 8 \ 9 \ 10 \ 1 \ 2 \ 3 \ 4 \ 5]$$

And apply the inverse of the parity check [H] matrix. The matrix Hstd and code word before it is transmitted codeword's (0's and 1's). The channel is memory less channel, and so the order of code word (0's and 1's) bits un important, a for easier option is to apply to original parity check matrix [H] to give parity check matrix

$$H' = \begin{bmatrix} 1 & 1 & 0 & 1 & 1 & 0 & 0 & 1 & 0 & 0 \\ 0 & 1 & 1 & 0 & 1 & 1 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 1 & 1 \\ 1 & 1 & 0 & 0 & 0 & 1 & 1 & 0 & 1 & 0 \\ 0 & 0 & 1 & 0 & 0 & 1 & 0 & 1 & 0 & 1 \end{bmatrix}$$

With the same properties as the parity check matrix [H] but which the transmitted same code word bits ordering as the matrix Hstd. Finally generate matrix G for the transmitted codeword's with parity check matrices is the Hstd and the parity check matrix H' is represented in the form of

$$G = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 1 & 1 & 0 & 1 \\ 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 & 0 & 1 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 \end{bmatrix}$$

The above Generator matrix G of the Processing in the form of off line process and the Generator matrix G is encoding and Parity check matrix H is decoding. The main drawback of coding and decoding approach is that, Parity check matrix H, the Generator matrix G will most

$$H_t = \begin{bmatrix} A & B & T \\ C & D & E \end{bmatrix},$$

The above matrix can be represented in T is lower triangular matrix

2. ERROR CORRECTS TECHNIQUES

The transmission errors basically are of two types namely the random errors and burst errors. The errors that occurs in a purely random manner are known as random errprs. Whereas the errors which occur in the form of bunches and hence which the not independent are known as the burst errors. Codes like binary BCH codes are useful in dealing with the random error. Because of impulse noise, more than one corresponding data bits change their state simultaneously. The error introduced in the received word is called as burst error.

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corresponding data bits change their state simultaneously. The error introduced in the received word is called as burst error.

2.1 Numerical Results by using LDPC Codes

The below Fig 3 and 4 Depict Is DISTORTION OUTAGE PROBABILITY and Performance of the power constraint (P) and out put power (Pout) for Dm=5db and Dm=2db.

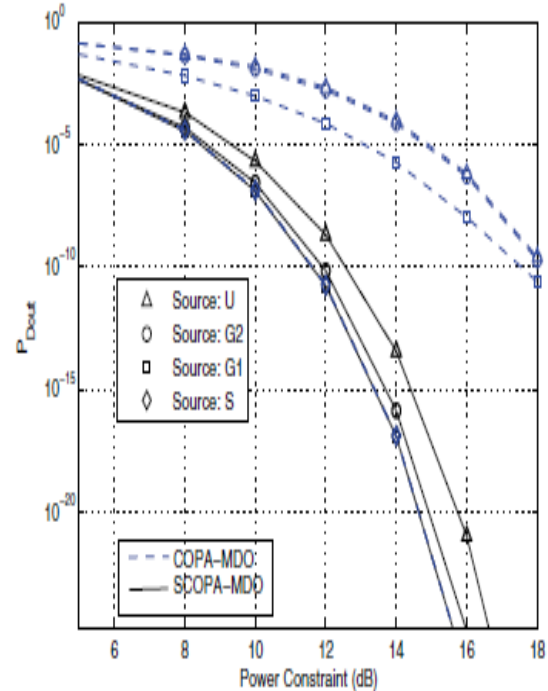


Fig.3 Distortion Outage probability vs power(P) provided by the SCOPA at the MDO and COPA at the MDO for the five main different sources: b= 1 and Dm = 8db.

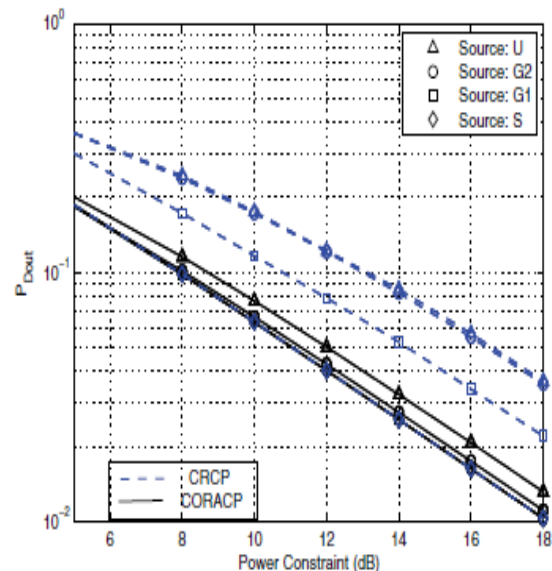
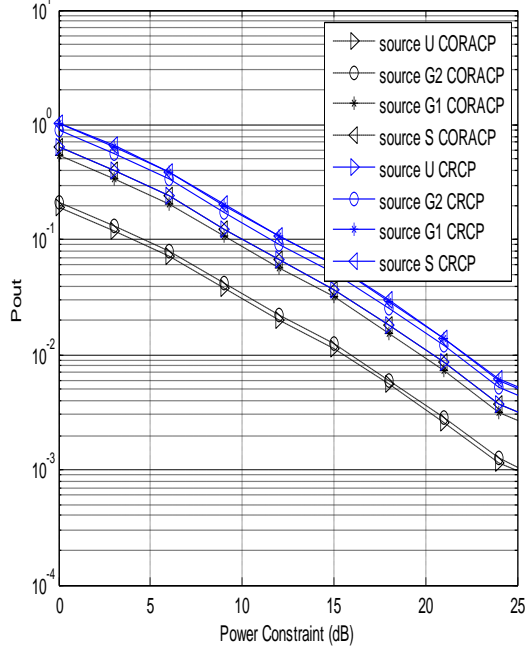


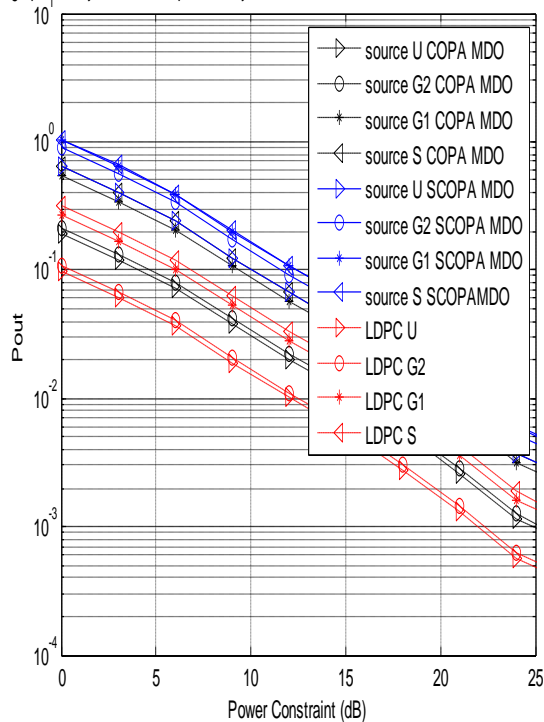
Fig. 4 is the distortion outage probability vs the power (P) and the provided by the CORACP nad the DRCP with for a five different sources: b=1db and Dm=8db.

2.2 Low Density Parity Check Codes (LDPC) Graphs

outage probability versus Pbar provided by CORACP and CRCP for five different sources; b = 1 ar



outage probability versus Pbar provided by CORACP and CRCP for five different sources; b = 1 ar



3. CONCLUSION

This paper is to introduce proposal and implementation for Low density parity checks (LDPC) codes are performance evaluation and design platform of LDPC codes. The Low density parity check (LDPC) codes are performance, simulation and design a platform of code words. This platform is to perform two main tasks. The first task is the performs parallel simulation and to the evaluate an Low density parity check (LDPC) codes performance.

The second task is, it graphical representations of the state of Low density parity check (LDPC) codes encoder and decoder. The encoder and decoder. The encoder and decoder are discussed with LDPC coding. The LDPC decoding may be best understand by the sum product algorithm and bit flipping algorithm showing the LDPC code can be implemented easily.

The simulation done for different rates are showing that are the short length LDPC codes are performing excellent, and the code perform better for all rates either low (or) high.

4. REFERNCES

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