

GaAs, InP, InGaAs, GaInP, p+-i-n+ Multiplication Measurements For Modeling of Semiconductor as Photo Detectors

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ABSTRACT

Optoelectronic is one of the thrust areas for the recent research activity. One of the key components of the optoelectronic family is photo detector to be widely used in broadband communication, optical computing, optical transformer, optical control etc. Present paper includes the investigation. carried on the basis of the. Multiplication measurements on GaAs, InP, InGaAs, GaInP, p+-i-n+s with μ -region thicknesses, with investigation of applicability of the local ionization theory. A local ionization coefficient to be increasingly unrepresentative of the position dependent values in the device as is reduced below 1 μ m. The success of the local model in predicting multiplication is therefore attributed to the dead-space information already being contained within the experimentally determined values of local coefficients. This suggested that these should therefore be thought of as effective coefficients, which, despite the presence of dead-space effects, can be, still be used with the existing local theory for efficiently quantifying multiplication and breakdown voltages.

Subject Descriptor

G-4 ,Matlab

General Term

Algorithms , Performance

Key words: Photo detectors, Impact ionization.

1. INTRODUCTION

With the advent of broadband communication, INTERNET communication, optical computing, the research in the optical devices has emerged as one of the thrust area for the active and consistent research. One of such primary devices is the photo detector. Therefore, recent years have seen tremendous research activities in the area of photo detector mathematical modeling and simulation. It includes the domain like optical control of r.f./microwave devices, broadband communication like optical communication, as a optoelectronic component to support green technology, etc.

Basic objective of the active research includes the reduction in the device dimension , reduction in power utilization, better performance against noise, ease of integration to make complete system on chip, better reliability against all possible

adverse condition and so on. At present the commercially available photo detectors for the various includes PIN diode, APD, photo transistor and the recent device like PIN FET. A photo detector operates by converting light signals that hit the junction to a voltage or current. The junction uses an illumination window with an anti-reflect coating to absorb the light photons. The result of the absorption of photons is the creation of electron-hole pairs in the depletion region. Impact Ionization is the process in a material by which one energetic charge carrier can lose energy by the creation of other charge carriers. For example, in semiconductors, an electron (or hole) with enough kinetic energy can knock a bound electron out of its bound state (in the valence band) and promote it to a state in the conduction band, creating an electron-hole pair. Three-five materials refer to compound semiconductors made from one element from Group III on the periodic chart (arsenic in the case of GaAs) and one from Group V (gallium in the case of GaAs). Other three-five (or III-V in Roman numerals) semiconductors include indium phosphide and gallium nitride. Band gap engineering is the process of controlling or altering the band gap of a material by controlling the composition of certain semiconductor alloys, such as GaAlAs, InGaAs, and InAlAs. It is also possible to construct layered materials with alternating compositions by techniques like molecular beam epitaxy. These methods are exploited in the design of heterojunction bipolar transistors (HBTs), laser diodes and solar cells.

The band gap energy of semiconductors tends to decrease with increasing temperature. When temperature increases, the amplitude of atomic vibrations increase, leading to larger inter atomic spacing. In a regular semiconductor crystal, the band gap is fixed owing to continuous energy states. In a quantum dot crystal, the band gap is size dependent and can be altered to produce a range of energies between the valence band and conduction band. It is also known as quantum confinement effect.

Band gaps also depend on pressure. Band gaps can be either direct or indirect, depending on the electronic band structure.

Gallium arsenide (GaAs)

A compound of the elements gallium and arsenic it is an III/V semiconductor, and is used in the manufacture of devices such as microwave frequency integrated circuits, monolithic microwave integrated circuits, infrared light-emitting diodes, laser diodes, solar cells, and optical windows.

Indium phosphide (InP)

A binary semiconductor composed of indium and phosphorus. It has a face-centered cubic crystal structure, identical to that of

GaAs and most of the III-V semiconductors InP is used in high-power and high frequency electronics because of its superior electron velocity with respect to the more common semiconductors silicon and gallium arsenide. It also has a direct bandgap, making it useful for optoelectronics devices like laser diodes. InP is also used as a substrate for epitaxial indium gallium arsenide based opto-electronic devices. Indium phosphide also has one of the longest-lived optical phonons of any compound with the crystal structure.

Indium gallium arsenide (InGaAs)

Indium gallium arsenide (InGaAs) is a semiconductor composed of indium, gallium and arsenic. It is used in high-power and high-frequency electronics because of its superior electron velocity with respect to the more common semiconductors silicon and gallium arsenide. InGaAs band gap also makes it the detector material of choice in optical fiber communication at 1300 and 1550 nm. Gallium indium arsenide (GaInAs) is an alternative name for InGaAs.

The indium content determines the two-dimensional charge carrier density. The optical and mechanical properties of InGaAs can be varied by changing the ratio of In and Ga, $\text{In}_x\text{Ga}_{1-x}\text{As}$. The InGaAs device is normally grown on an indium phosphide (InP) substrate. In order to match the lattice constant of InP and avoid mechanical strain, $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$, this composition has a cut-off wavelength of 1.68 μm .

Gallium indium Phosphide (GaInP)

GaInP, a wide band gap semiconductor lattice matched to GaAs, is of interest for a variety of device applications such as heterojunction bipolar transistors (HBTs) and heterojunction field-effect transistors (HFETs). The advantages of GaInP over GaAlAs for GaAs-based HBT applications include its large energy band gap (1.9 eV), lower conduction band offset, reduced deep-level concentration, and easier selective etching. GaInP-emitter HBTs with high current gain and good microwave performance have been reported.

The use of GaInP in the collector region of HBTs is also attracting attention because of its high breakdown voltage potential. For the collector material of high-voltage microwave HBTs, an important figure of merit is the product of breakdown electric field and carrier saturation velocity.

Which is roughly proportional to the product of breakdown voltage and current gain cutoff frequency attainable in the transistor. GaInP has a potentially higher breakdown field saturation velocity product than other materials lattice matched to GaAs.

2. THEORY

To design structures for high-voltage or high-power applications, an accurate knowledge of impact ionization coefficients in GaInP is necessary for calculating breakdown characteristics of junctions. In the work reported here, photocurrent multiplication was used to measure the electron and hole ionization coefficients in ~ 100 GaInP by illuminating

$p^+ - n^- - n^+$ diode structures from either side with above band gap radiation. The results show that GaInP has significantly lower values of α and β than those of GaAs or InP, a promising indication for high-voltage applications.

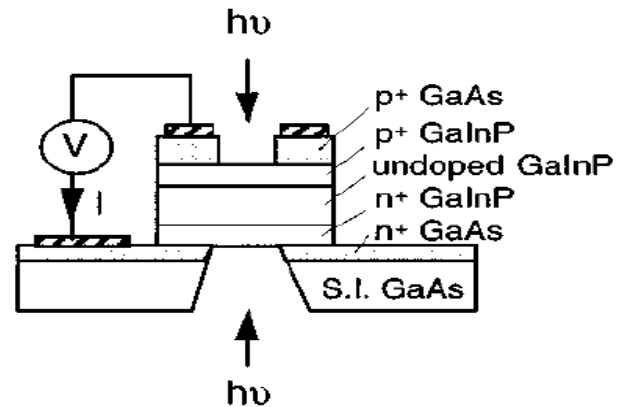


Figure.1. $p^- - n^- - n^-$ diode layer structure

Accurate determination of the electron and hole ionization coefficients, α and β respectively, is important, since these are used to determine avalanche multiplication characteristics and breakdown. Conventionally α and β are assumed to depend only on the local electric field, and the mean multiplication due to an electron-hole pair generated at position x is given by

$$M(x_0) = \frac{\exp \left[- \int_{x_0}^W (\alpha - \beta) dx \right]}{1 - \int_0^W \alpha \exp \left[- \int_0^x (\alpha - \beta) dx \right] dx}$$

As described by Stillman and Wolfe [7] where W is the total depletion width. The electric field exists between $x=0$ and $x=W$ causing electrons to move from left to right. This expression is also traditionally used to derive the values of α and β from photo multiplication measurements performed with carrier injection from the depletion region edges. [7]-[8]

For electron multiplication, M_e , $x_0 = 0$ and for pure hole multiplication M_h , $x_0 = W$. However, carriers entering the high field region with energy much less than the ionization threshold must traverse a dead space, distance d_e , for electrons or d_h for holes, before they acquire sufficient energy to impact ionize. With estimation this dead space distance is given by equating it to the ballistic distance a carrier requires to reach the ionization threshold energy, E_{th} i.e. $d = E_{th} / qF$.

For a device where electrons are injected at $x=0$, these corrections usually disallow electron ionization in a dead-space region from $0 < x < d_e$ and hole ionization in the region from $W - d_h < x < W$. Okuto and Crowell [9] presented an approximate expression relating multiplication to the ionization coefficients while accounting for the reduced multiplication by these regions. Bulman et al [10], simplified their expression to interpret the measured multiplication results from p-n-n junctions by assuming no electron-initiated

ionization occurs within a distance d_e from their injection point.

Physics of Impact Ionization

The impact ionization process in semiconductors has been observed and studied since 1950. In the past several decades, there has been more widespread interest in its applications due to the rapid progress in optical communication systems. Avalanche photo detectors (APDs) use the impact ionization principle to detect and amplify very low light signals and they are still used as the most sensitive detector for most systems. It is found that to have a high-gain, low-noise APD, the ionization rate for electrons and holes needs to be very different. Efforts to find the best material to perform impact ionization reveal that silicon has the largest ionization rate difference between electrons and holes among all semiconductors. Due to its cut-off wavelength around 1 μm , however, silicon has rarely been employed for today's optical communication systems that use 1.55 μm wavelengths light. Suitable materials using combinations of binary and ternary/quaternary III-V semiconductors such as InGaAs on InP substrates have provided solutions for longer wavelength detection.

Impact Ionization Gain Mechanism

The impact ionization gain mechanism can be demonstrated by using a thermally generated electron, or an electron created by an absorbed photon, travelling inside a semiconductor where a depletion region is formed. Figure 6 illustrates the impact ionization process in a reverse biased PIN. As shown in the figure, electrons can gain sufficient kinetic energy while travelling in a high electric field. If the electric field is high enough, this high-energy electron may initiate the electron-electron scattering so that an electron in the valence band can be excited to the conduction band. As a result of this, another electron-hole pair is produced by promoting an electron from the valence band into the conduction band. Due to the strong electric field, the subsequent electron and hole will continue to collide with the lattice and create more electron-hole pairs. Therefore, numerous carriers are generated and the result is a multiplied current output. This phenomenon is sometimes referred to as the avalanche breakdown

Ionization Threshold Energy

The minimum energy required to excite an electron, however, is more than just freeing an electron from the valence band. In order to decide the ionization threshold energy, various methods including from the valence to the conduction band is equal to the band gap energy of the semiconductor. Impact ionization parabolic, non parabolic, realistic, and non local pseudo potential band structure have been utilized. It is found that the threshold energy differs for most semiconductors. It is a function of the band structure, effective mass ratio between electron and hole, density of state, phonon interaction, and spin-orbit splitting energy.

The best way to estimate the ionization threshold energy involves a simple two parabolic band model. In this model, we consider one conduction band with effective mass m_e and one valence band with mass m_h . As shown in Fig. 6, prior to the collision, the electron travelling from the left-hand side has a kinetic energy of $\frac{1}{2} m_e v_i^2$ and a momentum of $m_e v_i$, where v_i is the initial velocity of the electron. After collision, three carriers exist: a new electron-hole pair plus the original

electron. Electrons continue travelling to the right and the hole to the left.

Assuming that the collision is elastic, the conservation of energy and momentum must be satisfied these two assumptions are summarized as

$$\frac{1}{2} m_e v_t^2 = E_g + \left(\frac{1}{2} m_e v_e^2 \times 2 + \frac{1}{2} m_h v_h^2 \right) \quad (1)$$

And

$$m_e v_t = m_e v_i \times m_h v_h \quad (2)$$

Where E_g is the energy band gap of the semiconductor, v_e is the electron velocity, and v_h is the hole velocity after the collision. Note that when $m_e = m_h$ and $v_e = v_h = v_i$, (1) and (2) can be derive that the required initial electron energy for ionization process as

$$E_i = \frac{1}{2} m_e v_i^2 = 1.5 E_g \quad (3)$$

This is the well-known 3/2-band-gap rule for ionization threshold energy in semiconductor [1].

Ionization Coefficients and Gain Equations

To derive the gain equations for impact ionization process, we need to first define the impact ionization coefficient. For most semiconductor materials, the impact ionization process is asymmetric to some degree for electrons and holes. For example, the ionization rate for electrons is about 5 times greater than holes in Germanium. For silicon, however, electrons can ionize 50 times to 1000 times more readily than holes depending on the electric field amplitude. The probability for initiating impact ionization is quantified as the impact ionization coefficient. It is defined as the reciprocal of the average distance travelled by an electron or hole to produce an electron-hole pair. Therefore, its unit is cm^{-1} . For electrons, the coefficient is denoted as α . For holes, it is denoted as β . Materials with very different value for α and β , such as in silicon can create less impact ionization noise because only one type of carrier is dominant during the ionization process. First, the length of the semiconductor is long ($L > 1 \mu\text{m}$) such that non-local theories [4] are not considered. Secondly, the current density is low to avoid the space charge effect which may screen the electric field and lower the gain. A differential equation can be used to describe the multiplication process in terms of ionization rates α and β . If β is much smaller than α , (4) can be rewritten as

$$M_n = \frac{1}{1 + \frac{\alpha}{\alpha - \beta} [\exp(-x(\alpha - \beta))]_0^L} = \frac{(\alpha - \beta) \exp(L(\alpha - \beta))}{\alpha - \beta \exp(L(\alpha - \beta))}$$

(4)

$$M_p = \frac{1}{1 + \frac{\beta}{\beta - \alpha} [\exp(-x(\beta - \alpha))]_0^L} = \frac{(\beta - \alpha) \exp(L(\beta - \alpha))}{\beta - \alpha \exp(L(\beta - \alpha))}$$

(5)

$$M_n \approx \frac{\exp(L\alpha)}{1 - \frac{\beta}{\alpha} \exp(L\alpha)}$$

(6)

A positive feedback factor (β/α) in (6) shows the effect of ionization coefficients on multiplication gain. As β gets bigger, but still much smaller than α , M_n can reach breakdown in a shorter distance.

If we consider an extreme case, $\beta = 0$, (2.6) can be further simplified to

$$M_n = \exp\left(\int_0^L \alpha dx\right) = \exp(\alpha L)$$

(7)

From (7) we observe that when $\beta = 0$, there is no avalanche breakdown because M_n just continues to increase exponentially with αL . A plot is given in Fig.2 using (6) to demonstrate the gain versus αL for various value of (α/β)

Impact Ionization Coefficient Measurement

The discussion above reveals the relation between carrier multiplication gain and ionization coefficients. In order to find out the ionization coefficients for electron and hole, experimental measurement is necessary because there is no good model for calculating these values. We know that α and β are strong function of electric field in the multiplication region. Electric field, however, also depends on the bias voltage, doping profile, and the device geometry. In this section, we will explain how to measure the ionization coefficients accurately for different device structures including p-i-n diodes.

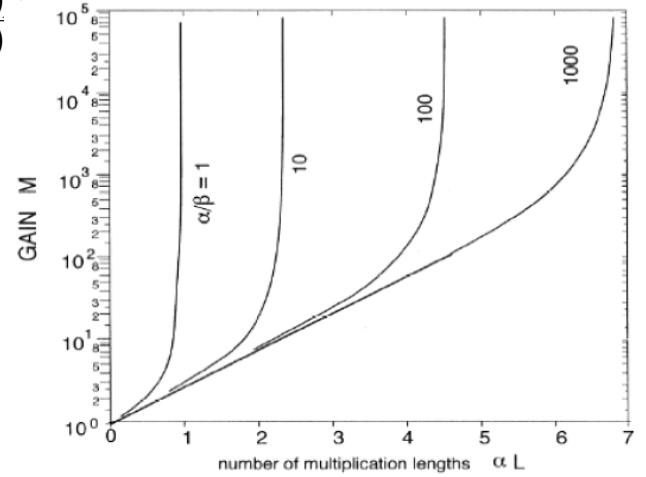


Figure 2 - Multiplication gain M versus αL for pure electron injection. Various α/β value are used to demonstrate its effect on avalanche breakdown curve

In measurement, a very high energy light is used to illuminate the diode. Due to the high energy of the photons, electron-hole pairs can be created very close to the surface to obtain the pure electron or hole injection condition. Electron ionization is measured by shining light from the p+ side. Photon-excited electrons are injected into the intrinsic layer while holes are swept to the left. Hole ionization is measured by illuminating light from the n+ side, injecting holes into the intrinsic layer.

The electron gain M_n can be calculated by dividing the total current measured for a given electric field by the photocurrent created by the light. The photocurrent can be precisely measured when the diode is operated without avalanche gain.

The hole gain M_p can be obtained through the same process.

Once M_n and M_p are known, we can use equation (4) and (5) to derive formulas related α and β to M_n and M_p . They are calculated to be

$$\alpha = \frac{1}{L} \left(\frac{M_n - 1}{M_n - M_p} \right) \ln \left(\frac{M_n}{M_p} \right)$$

(8)

And

$$\beta = \frac{1}{L} \left(\frac{M_p - 1}{M_p - M_n} \right) \ln \left(\frac{M_p}{M_n} \right)$$

(9)

Impact Ionization Response

The carrier build-up time in the multiplication process depends on the contribution of carrier feedback. In the ideal condition, where only one type of carrier is capable of initiating impact ionization process (electrons for $\beta = 0$), the output current pulse increases with the transit time for the initially injected electron. The output current decreases to zero as all the ionized holes arrive at the negatively biased electrode. Thus, the current pulse lasts about twice as long as the transit time. Since the pulse

width is independent of the multiplication gain, there is no gain-bandwidth product limitation when β or $\alpha = 0$.

It is found that the avalanche multiplication process does not affect the device bandwidth as long as the dc multiplication gain M is less than α/β . On the other hand, if $M > \alpha/\beta$, the multiplication gain becomes a function of frequency and is expressed as

$$M(\omega) = M_0 / (\omega^2 M_0^2 \tau_1^2)^{1/2}$$

(10)

Where τ_1 is an effective transit time and is approximately $\tau_1 = N(\beta/\alpha)\tau$. N is a number varying slowly from 1/3 to 2 as β/α varying from 1 to 10^{-3} , and τ is the transit time equal to L/v_s where L is the length of the avalanche gain region and v_s is the saturation velocity

The gain-bandwidth product for $M > \alpha/\beta$ can be obtained using (10) for high frequencies and is expressed as

$$M(\omega) \times \omega = \frac{M_0 \omega}{(\omega^2 M_0^2 \tau_1^2)^{1/2}} = \frac{1}{\tau_1} = \frac{1}{N(\beta/\alpha)\tau} \quad (11)$$

Equation (11) indicates the basic requirements for an impact-ionization based device to obtain a high gain-bandwidth product. These requirements include a small β/α value and short intrinsic time. Therefore, a correct choice of material, multiplication layer thickness, and carrier transport velocity are essential.

Using (10), the calculated frequency response for a P-I-N structure with $1\mu\text{m}$ thick gain region when operating at $M=50$ is given in Figure 3.

Using Matlab to simulate the both the measure of α and β since Bulmanet al [10]. found that $\beta > \alpha$, these two sets of parameters for the coefficients since they enable the data to be more accurately quantified over the wide field range the coefficients are parameterized in The impact ionization coefficients can be fitted into an exponential form:

$$\alpha(E) = A_e \exp(-B_e / E)$$

$$\beta(E) = A_h \exp(-B_h / E)$$

$$\alpha_{eH} = A_{eH} \exp[-(B_{eh} / \xi)^{C_{eff}}]$$

$$\beta_h = A \exp[-(B / \xi)^C]$$

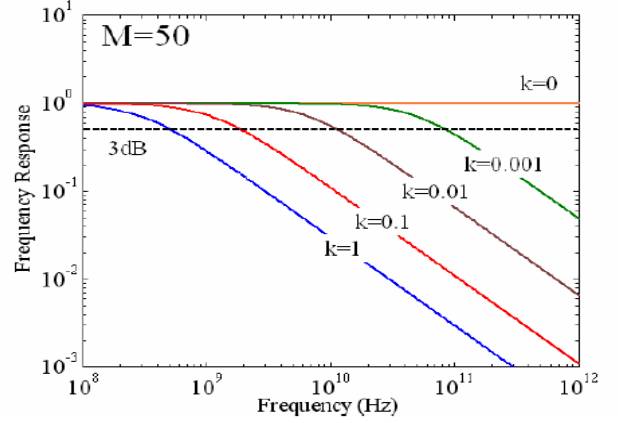


Figure 3: Frequency Response of a P-I-N photodiode with $1\mu\text{m}$ thick multiplication layer. Notice that parameter k is define as β/α

For the local calculation the values of α and β were taken from [10] for electric fields, F . This investigation involved measuring both the multiplication and excess noise characteristics of several samples with overlapping field regions and represents the most extensive and rigorous to date. By developing a model for the ionization coefficient that was independent of the multiplication-region width, each carrier and each material, a single electric-field-dependent model for the ionization coefficient was developed that was suitable for devices of all thicknesses

Impact Ionisation coefficient and Multiplication of electrons & holes for GaAs

Impact Ionisation Coefficients for GaAs

$$\alpha(E) = 1.89 \times 10^7 [(\exp(-(5.75 \times 10^7) / E))^{1.82}] \text{cm}^{-1}$$

$$\beta(E) = 2.21 \times 10^7 [(\exp(-(6.57 \times 10^7) / E))^{1.75}] \text{cm}^{-1}$$

Multiplication of electrons and holes for GaAs

Simulation of calculate Electron and hole multiplication factors for a range of ideal p-i-n s with from $1\mu\text{m}$ down to $0.48\mu\text{m}$ for GaAs with temperatures of 20K to 500K. Shown in figure 5 increasing temperature causes the multiplication of electrons and holes to shift to higher voltage therefore higher electric fields are required to offset the increase in carrier cooling by phonon scattering and maintain multiplication

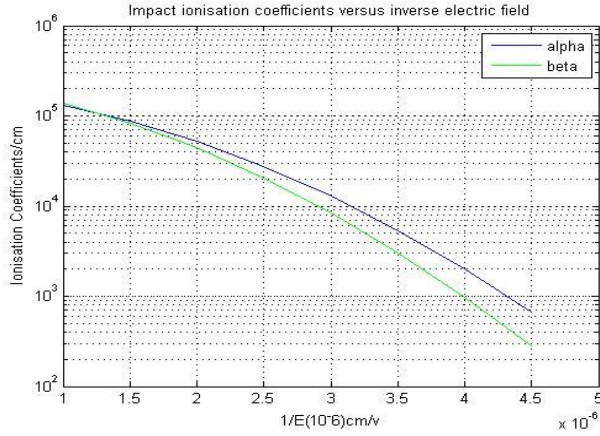


Figure 4: Impact Ionisation Coefficients for GaAs versus inverses electric field

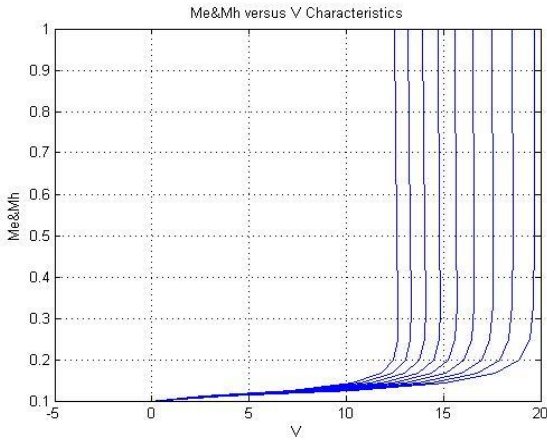


Figure 5: simulated of multiplication $w = 0.48\mu m$ device as a function of temperature from 20K to 500K

Impact Ionisation coefficient and Multiplication of electrons & holes for InGaAs

$$\alpha_{eH} = A_{eH} \exp[-(B_{eH} / \xi)^{C_{eH}}]$$

$$\beta_h = A \exp[-(B / \xi)^C]$$

Impact Ionisation Coefficients for InGaAs (narrowband gap semiconductor) great importance in the case of an ionization coefficient which increases with temperature that can result in an unstable positive power dissipation feedback which the voltage breakdown can be obtained by extrapolating the multiplication curve

As shown in fig 6

For alpha

$$A_{eH} = 7.2597 \times 10^4 - (24.204T) + (0.3259T^2) cm^{-1}$$

$$B_{eH} = 5.9988 \times 10^5 + (3.4763 \times 10^2 T) + (2.4768T^2) V/cm$$

$$C_{eH} = 1.783 + (7.2548 \times 10^{-4} T)$$

For beta

$$A = 6.1026 \times 10^5 + (9.6637 \times 10^2 T) + (1.1384T^2) cm^{-1}$$

$$B = 1.3394 \times 10^6 + (1.0699 \times 10^3 T) + (20.4507T^2) V/cm$$

$$C = 1.0910 - (2.3505 \times 10^{-4} T)$$

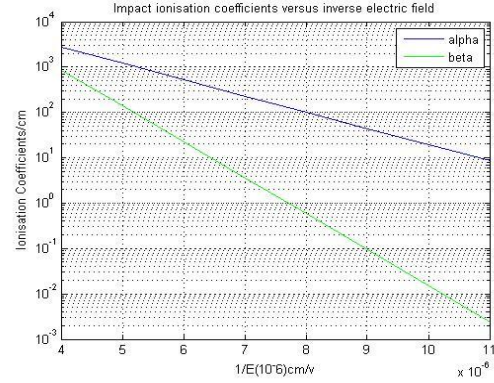


Figure 6: Impact Ionisation Coefficients for InGaAs versus inverses electric field

Multiplication of electrons and holes for InGaAs

Simulation of Electron multiplication factors that depends on temperature with the thickness of 1.3um and 1.9 um for InGaAs p-i-ns from 20K to 400K and hole multiplication with thickness of 3.0um as n-i-p from 20K-300K.

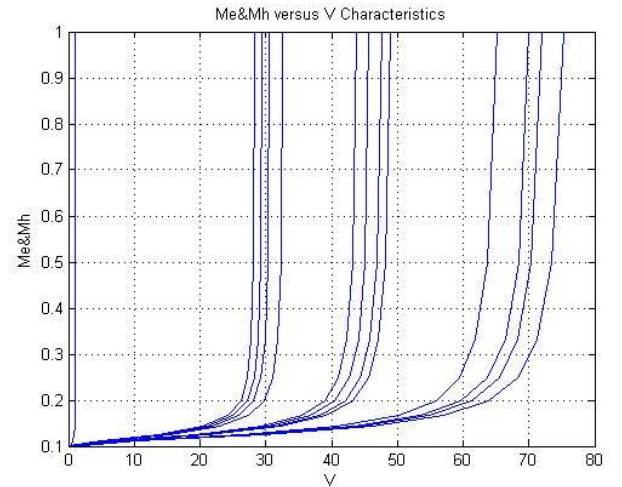


Figure 7: Simulated of multiplication characteristics of 1.3 and 1.9 um thick InGaAs p-i-n diode from 20–400 K

The results from figure 7 show a very limited increase in photocurrent initially and then the sudden and clear onset of the avalanche multiplication process. The avalanche multiplication of all the p-i-n and n-i-p structures clearly decreases with increasing temperature, indicating a negative temperature dependence of electron and hole ionization coefficients respectively

Impact Ionisation coefficient and Multiplication of electrons & holes for InP

The electron and hole ionization coefficients can be extracted from the measured multiplication results if both electrons initiated and hole initiated multiplication results are available for the same structure When an electron (or hole) initiates the

multiplication process, an electric current is induced by the moving electrons and holes within the multiplication region

InP has high electron peak velocities resulting from large inter valley separation and good breakdown properties owed to a relatively low electron impact ionization coefficient

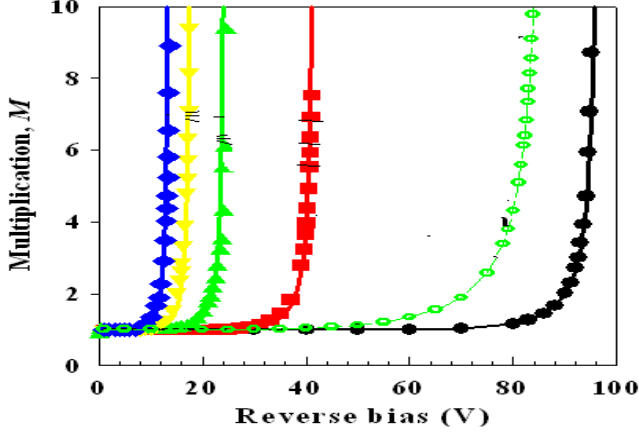


Figure 8: Measured M_e (symbols) Calculated M_e (solid lines) using bulk ionization coefficients for InP [19]

Compared to the data of GaAs and InP illustrated in Fig.6 and Fig. 9, GaInP has the lowest values of α and β , signifying a higher breakdown voltage. Moreover, the slopes of the curves of α and β vs $1/E$ for GaInP shown in Fig. 7 are steeper than for the others.

Impact Ionisation Coefficients for InP

$$\alpha(E) = 2.93 \times 10^6 [\exp(-(2.64 \times 10^6)/E)] \text{ cm}^{-1}$$

$$\beta(E) = 1.62 \times 10^6 [\exp(-(2.11 \times 10^6)/E)] \text{ cm}^{-1}$$

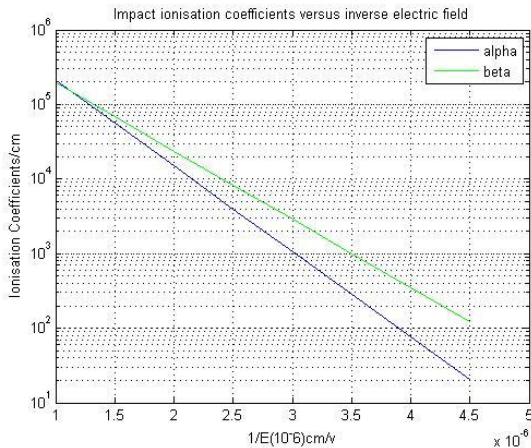


Figure 9: Impact Ionisation Coefficients for InP versus inverse electric field

Multiplication of electrons and holes for InP

The results from figure 10 show that the temperature dependence of multiplication is not uniform, since the multiplication curves with thickness of the materials from 0.24 to 2.40um respectively. Simulation of Electron multiplication factors for a range of ideal p-i-ns with from 0.24um down to 2.40um for InP with temperatures of 20K to 500K

As a test of the parameterized coefficients, test shows for all the materials, the predicted multiplication for an ideal diode, compared with the measured characteristic. It can be seen that

the agreement is good, both at low and high multiplications using matlab for these simulations for wideband gap semiconductor and narrowband gap semiconductor.

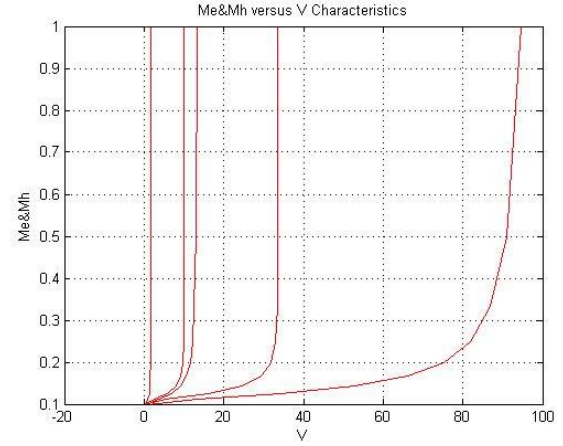


Figure10: Simulated of multiplication characteristics of 0.24 and 2.40 um thick for InP p-i-n diode from 20–400 K Impact Ionisation coefficient and Multiplication of electrons & holes for GaInP

The structures for high-voltage or high-power applications, an accurate knowledge of impact ionization coefficients in GaInP is necessary for calculating breakdown characteristics of junctions. In the work reported here, photocurrent multiplication was used to measure the electron and hole ionization coefficients from either side with above bandgap radiation. The results show that GaInP has significantly lower values of α and β than those of GaAs or InP, a promising indication for high-voltage applications.

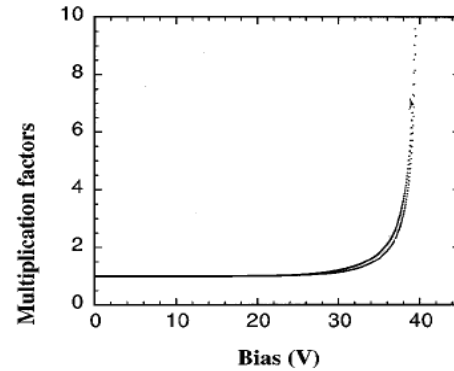


Figure 11: $p^+ - n^- - n^+$ diode layer structure and the measured results of M_n and M_p as a function of reverse bias.[20]

Impact Ionisation Coefficients for GaInP(wideband gap semiconductor)

$$\alpha(E) = 3.85 \times 10^6 [\exp(-(3.17 \times 10^6)/E)] \text{ cm}^{-1}$$

$$\beta(E) = 1.71 \times 10^6 [\exp(-(3.19 \times 10^6)/E)] \text{ cm}^{-1}$$

The electron and hole multiplication factors, M_n and M_p , are defined as the total output photocurrent divided by the electron or hole current injected at the contacts. Results for M_n and M_p from respective measurements are shown in Fig 11.

In these results, slight corrections were made to the injected currents due to slightly voltage-dependent intrinsic reverse currents. Because the widths of the depletion regions depend on bias, the number of thermally generated minority carriers that diffuse into the depleted n^- region also varies. This leads to a slight increase of the injected current as the reverse bias is increased

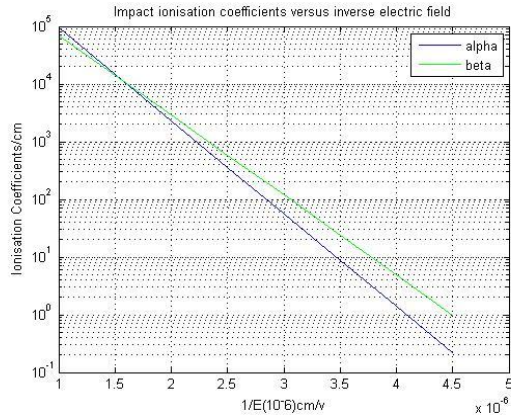


Figure 12: Impact Ionisation Coefficients for GaInP versus inverse electric field

Multiplication of electrons and holes for GaInP

Simulation of Electron multiplication factors for a range of ideal p-i-n s with from 0.24um down to 2.40um for GaInP with temperatures of 300K

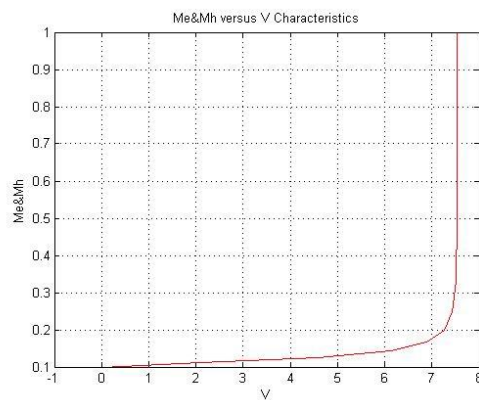


Figure 13: Simulated of multiplication characteristics of 0.24 um thick for GaInP p-i-n diode from 300 K

3. RESULT AND DISCUSSION

Voltage breakdown due to avalanche multiplication, also defined as the reverse bias voltage where multiplication rate goes to infinity naturally of great practical interest.

The voltage VB is defined as the reverse-bias voltage across the multiplication region at which the mean gain becomes infinite.

In doing so rather than testing with hands on materials in fab or clean room physically testing the materials, with this simulation we can create a model and simulate any materials first with respect to their voltage breakdown versus thickness (width) without wasting unnecessary time and cost for testing of materials

Case 1

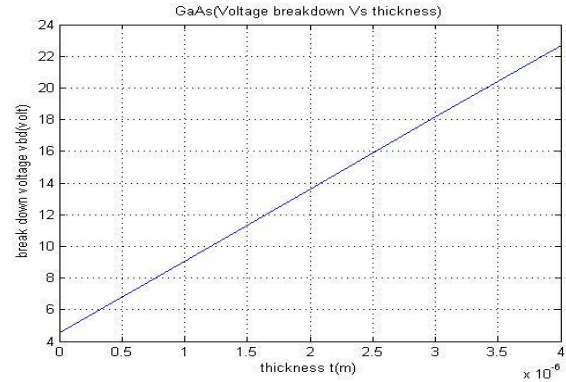


Figure 14: Simulated of voltage breakdown versus thickness for GaAs

GaAs wide band gap, highly resistive which makes it a very good electrical substrate therefore makes it a very good material for ideal material for microwave and millimeter wave integrated circuits. It can also be operated at higher power levels because they have higher breakdown voltage.

Case 2

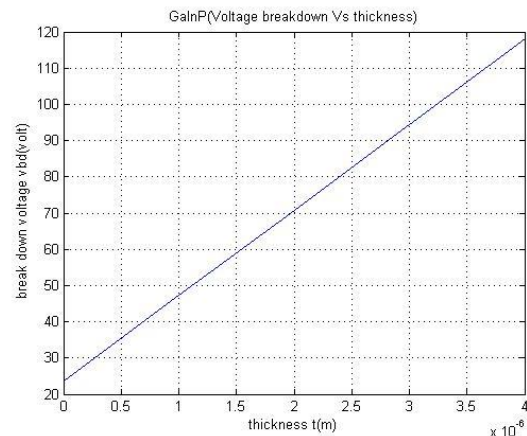


Figure 15: Simulated of voltage breakdown versus thickness for GaInP

GaInP wideband gap semiconductor exhibits high breakdown voltage characteristics indicating that it is a good choice of material for high power applications. This suggests that the ideal electric field distribution assumption is valid and that edge effects are not important in this material system for mesa geometry structures. Note that since these punches through

devices have a low carrier concentration in the n^- region and high breakdown voltage, the effects of dead space could be ignored. α And β data were used to calculate the expected breakdown voltages for p-i-n diode with various thicknesses

Case 3

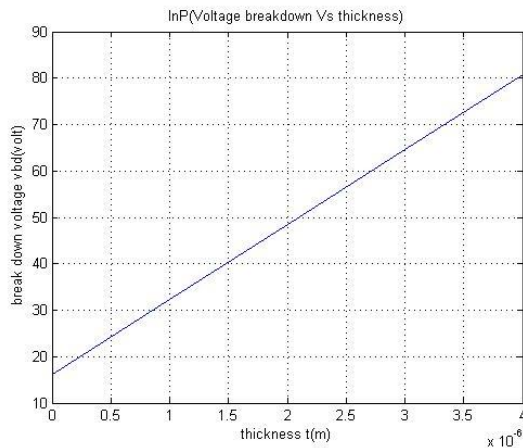


Figure 16: Simulated of voltage breakdown versus thickness for InP

InP wideband gap have an advantage compared to GaAs for many applications when used in high-field regions of the device profile, can significantly improve device performance. These applications include high performance power amplifier for cellular phones, ultra-efficient ultra-linear power amplifiers ideally suited for digital communication systems and satellite networks ICs, and highly integrated mixed signal and high-speed fiber-optic circuits. Wide bandgap semiconductors are associated with a high breakdown voltage. This is due to a larger electric field required to generate carriers through impact mechanism.

Case 4

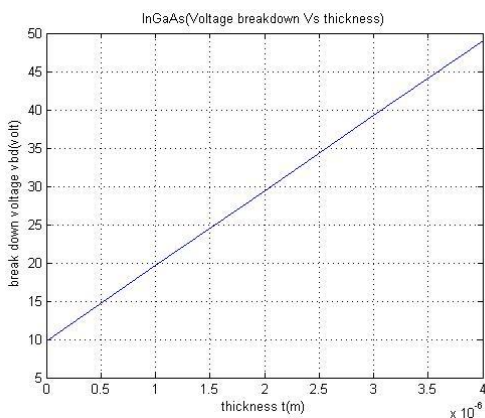


Figure 17: Simulated of voltage breakdown versus thickness for InGaAs

InGaAs with narrow band gap is also a popular material in infrared detectors and some short wave infrared cameras. It also has lower multiplication noise than germanium when used as the active multiplication layer of an avalanche photodiode. Also has a low breakdown voltage

4. CONCLUSION

For this research work materials are simulated base on p-i-n diodes therefore very little studies on Avalanche Photodiode (APD) and Schottky Barrier diodes.

The carriers are absorbed in a π - region. The absorption leads to the generation of electron-hole pairs in this region. The electric

field in the π -region is high enough to separate the carriers. The electric field across the π -region is not high enough for the charge carriers to gain enough energy for multiplication to take place.

The electric field, however, in the n+ p- region the electric field is significantly higher so that the charge carriers (in this case electrons only) are strongly accelerated and pick up energy

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